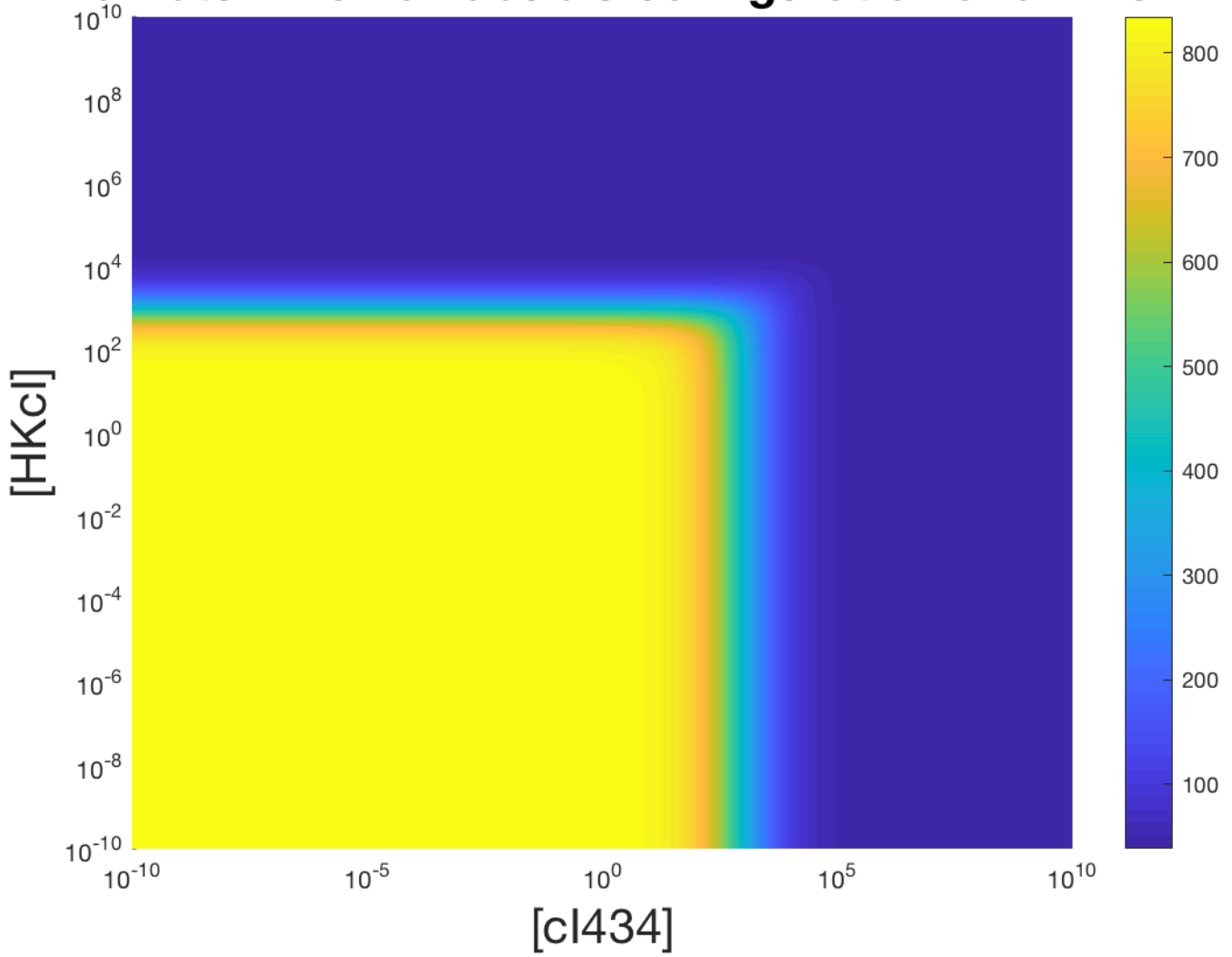
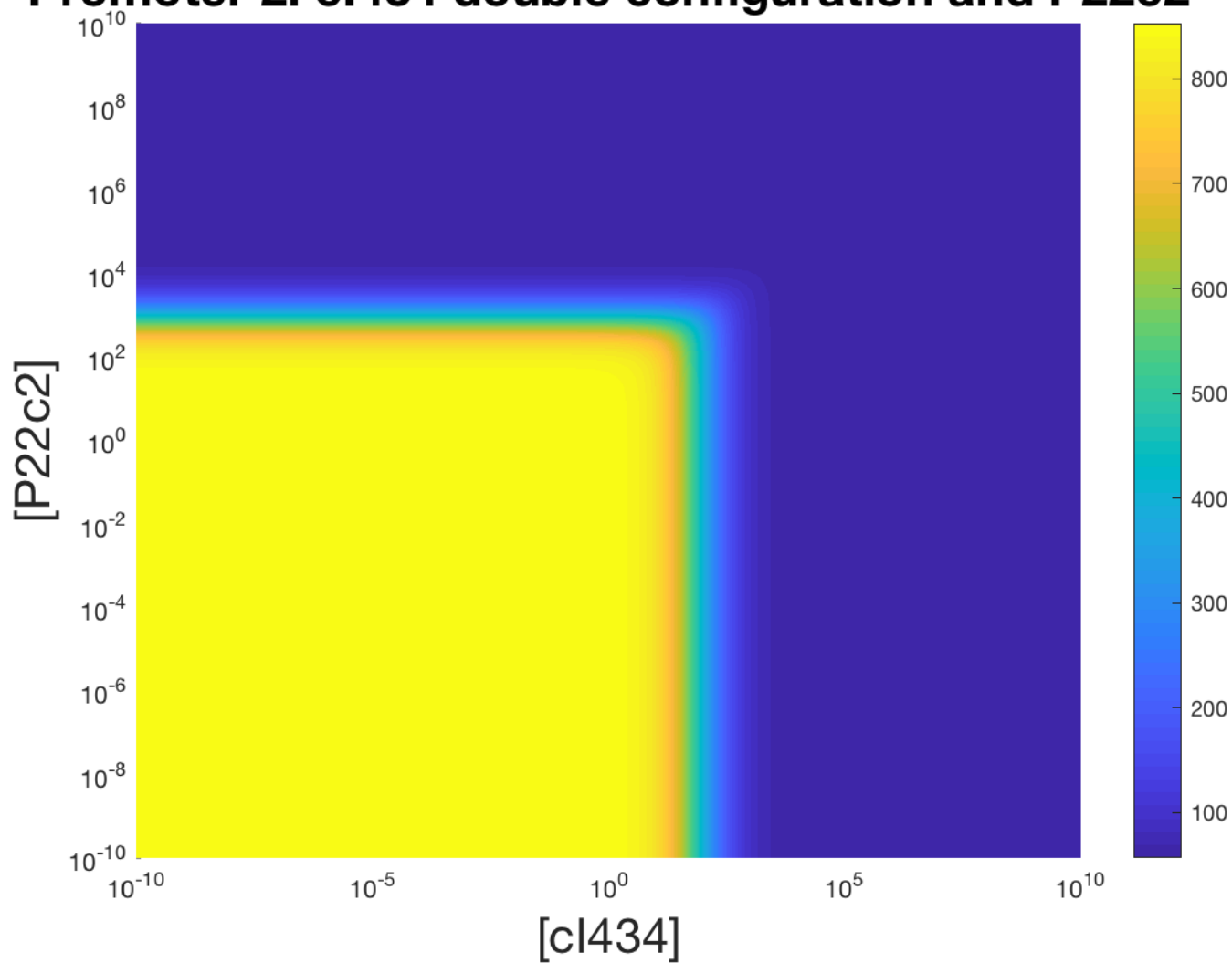


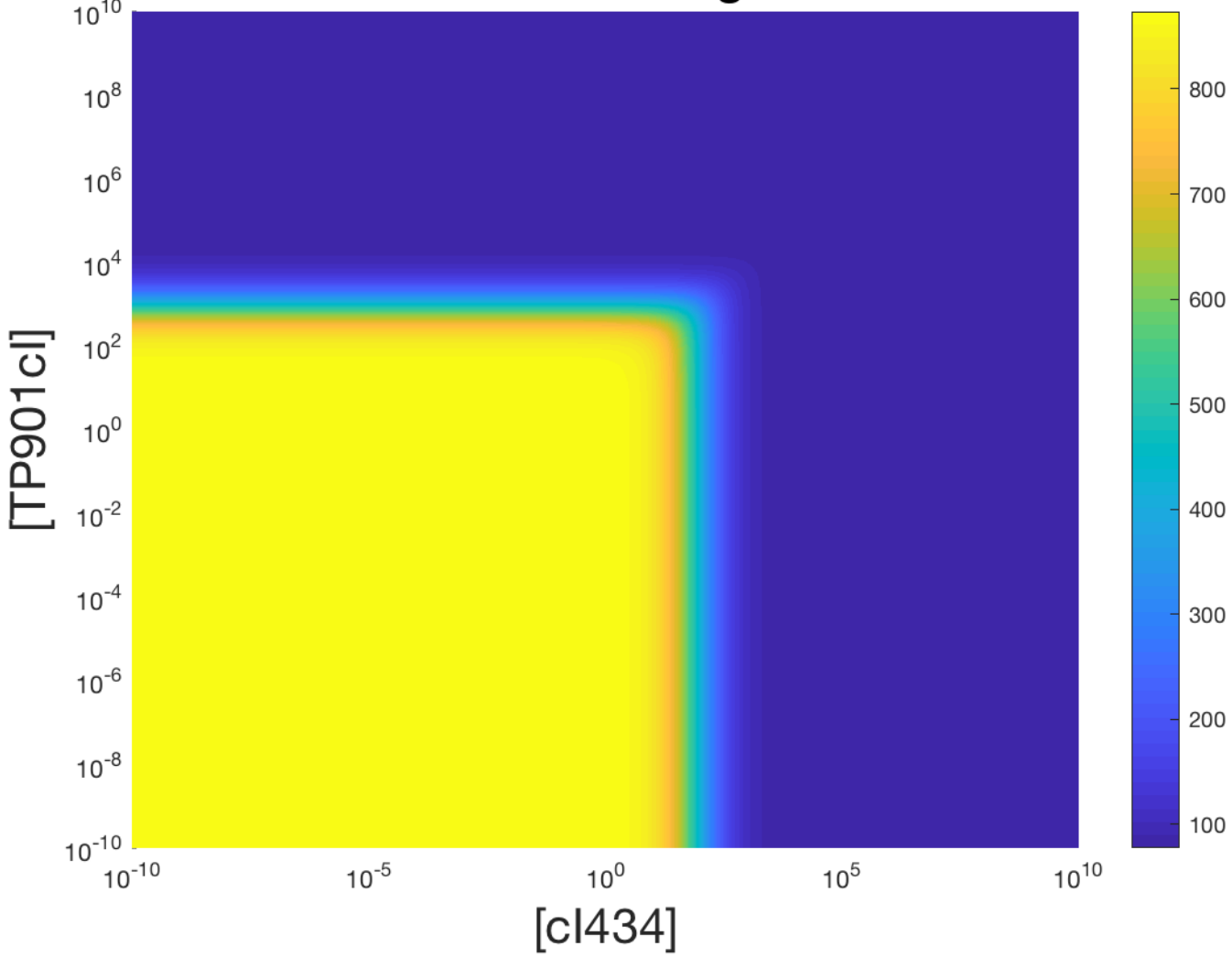
# Promoter 1: cl434 double configuration and HKCl



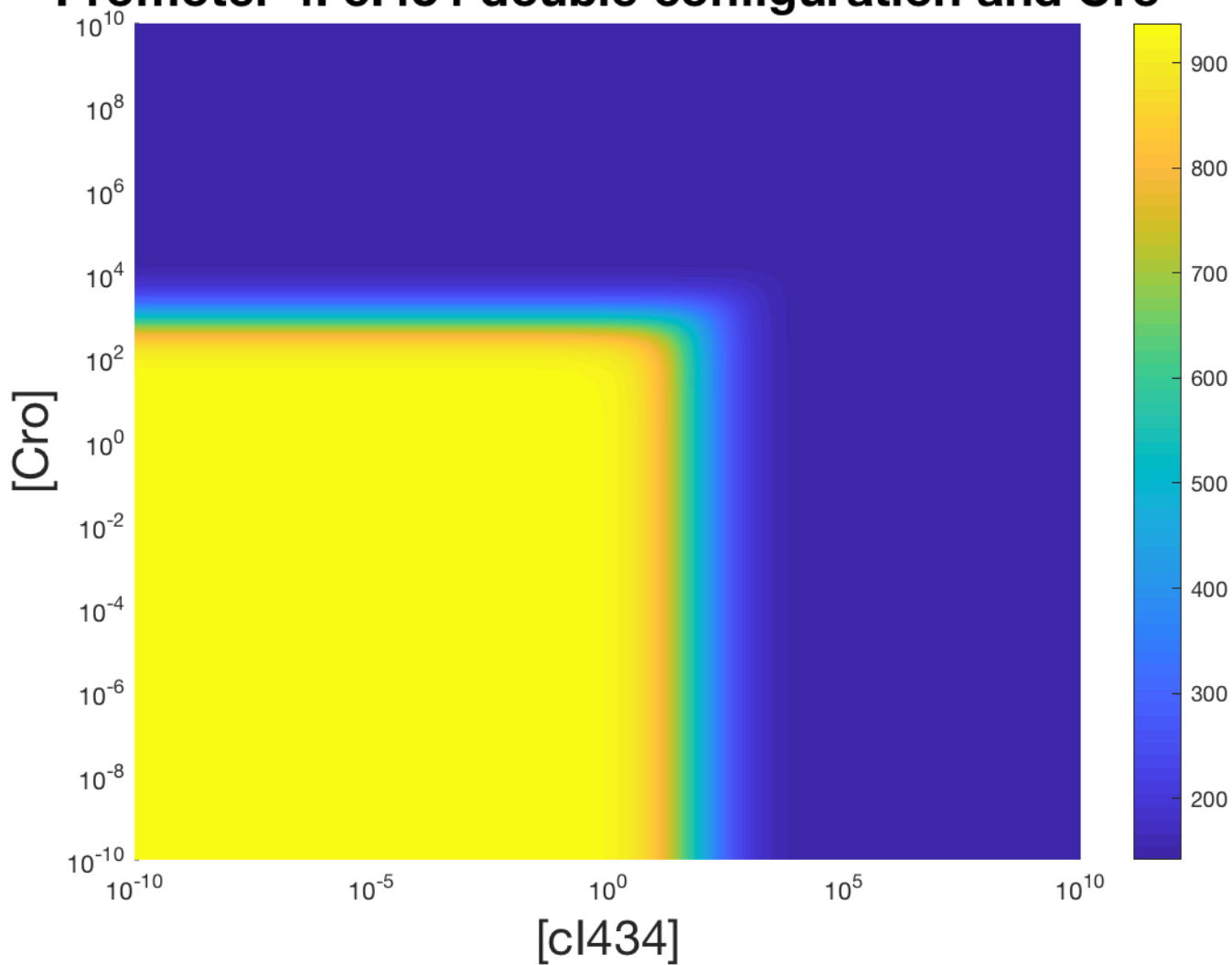
## Promoter 2: cl434 double configuration and P22c2



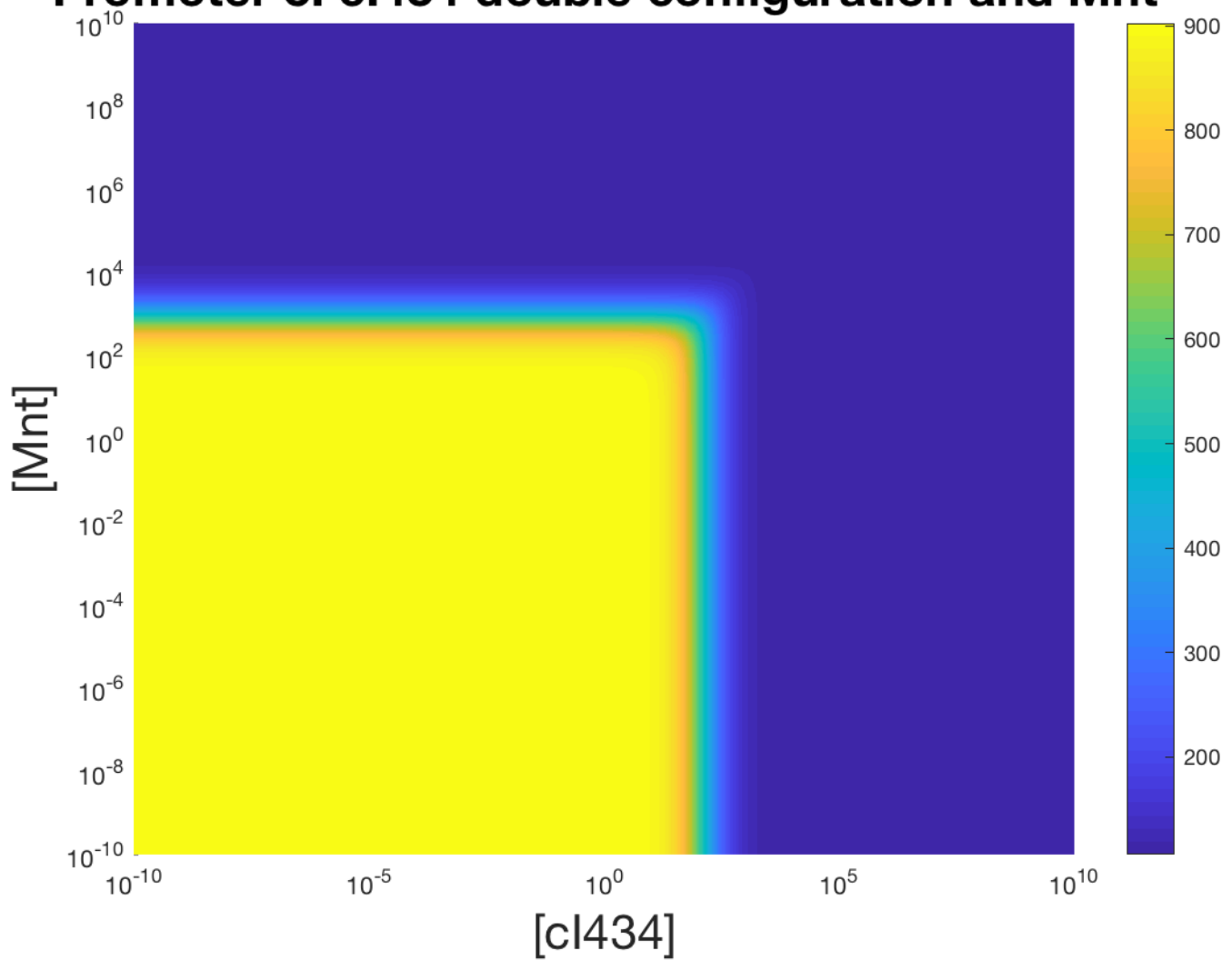
# Promoter 3: cl434 double configuration and TP901cl



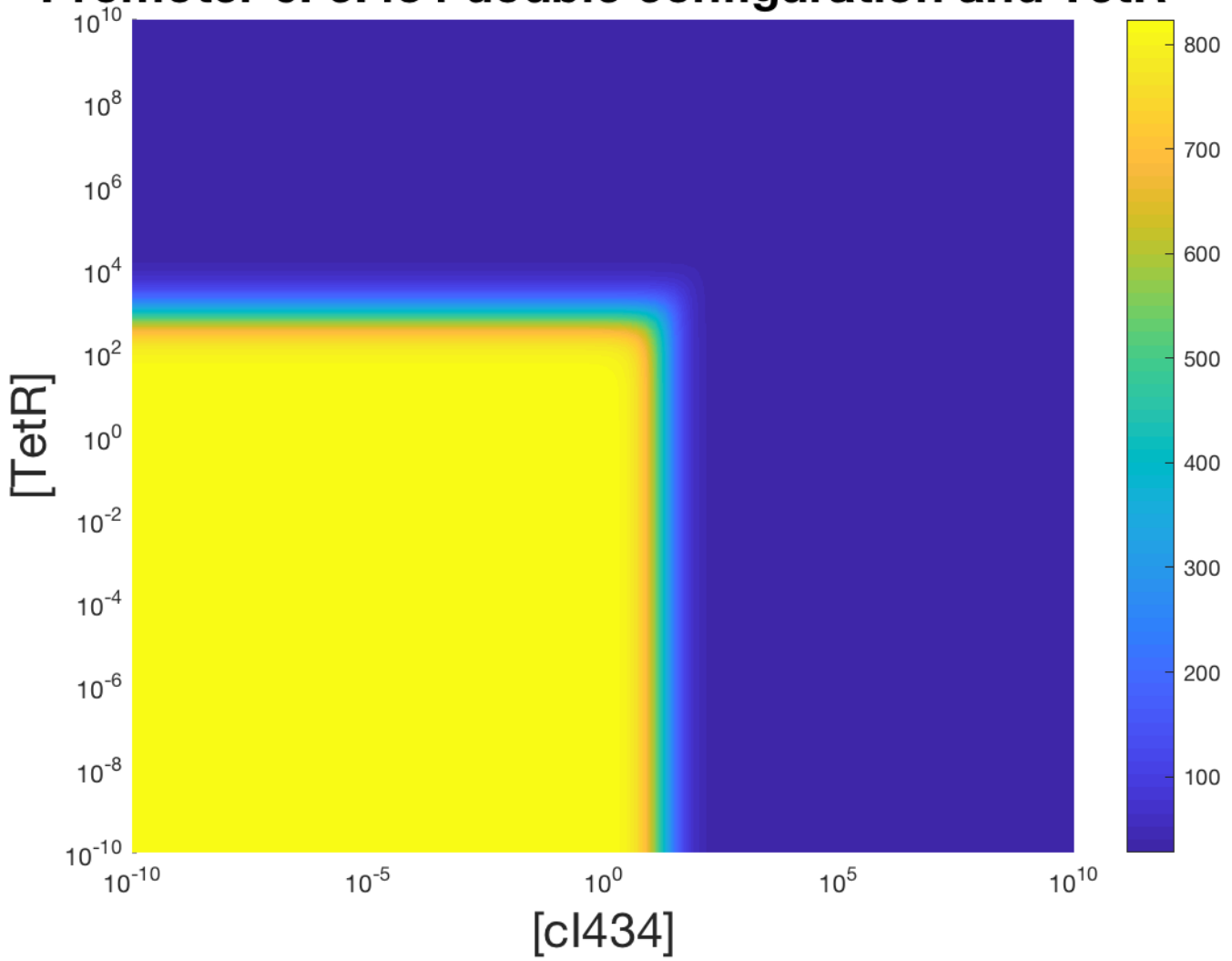
# Promoter 4: cl434 double configuration and Cro



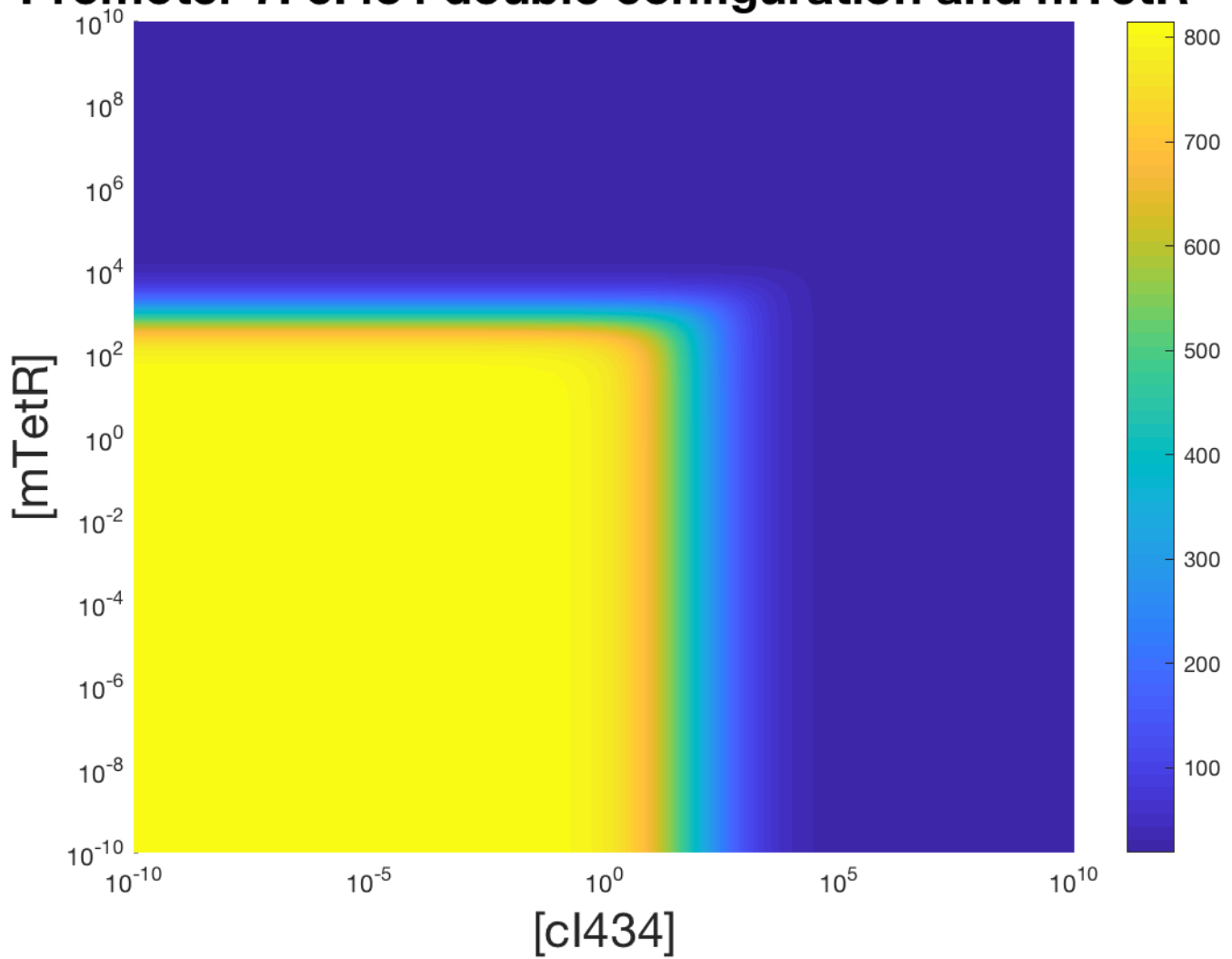
## Promoter 5: cl434 double configuration and Mnt



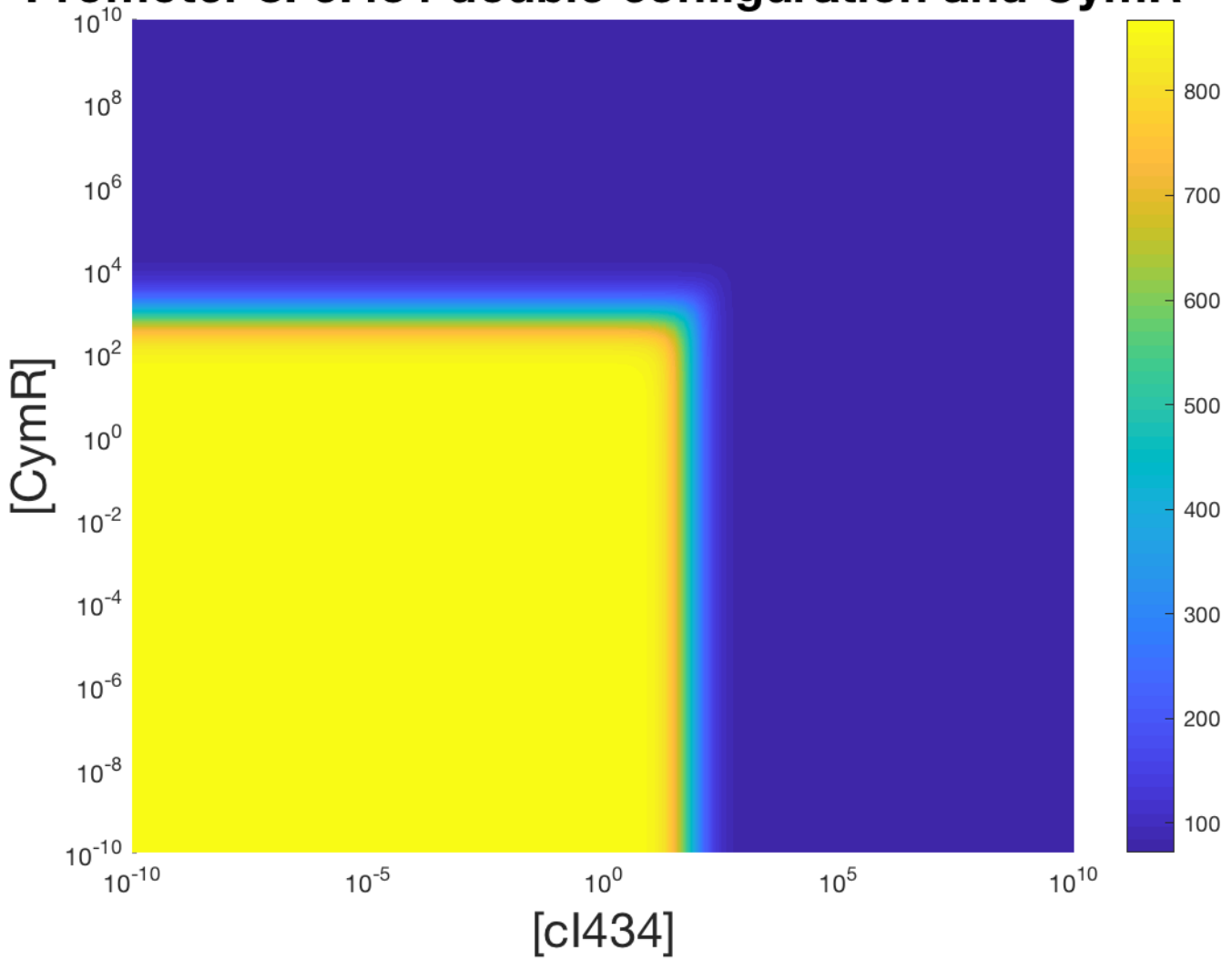
## Promoter 6: cl434 double configuration and TetR



## Promoter 7: cl434 double configuration and mTetR

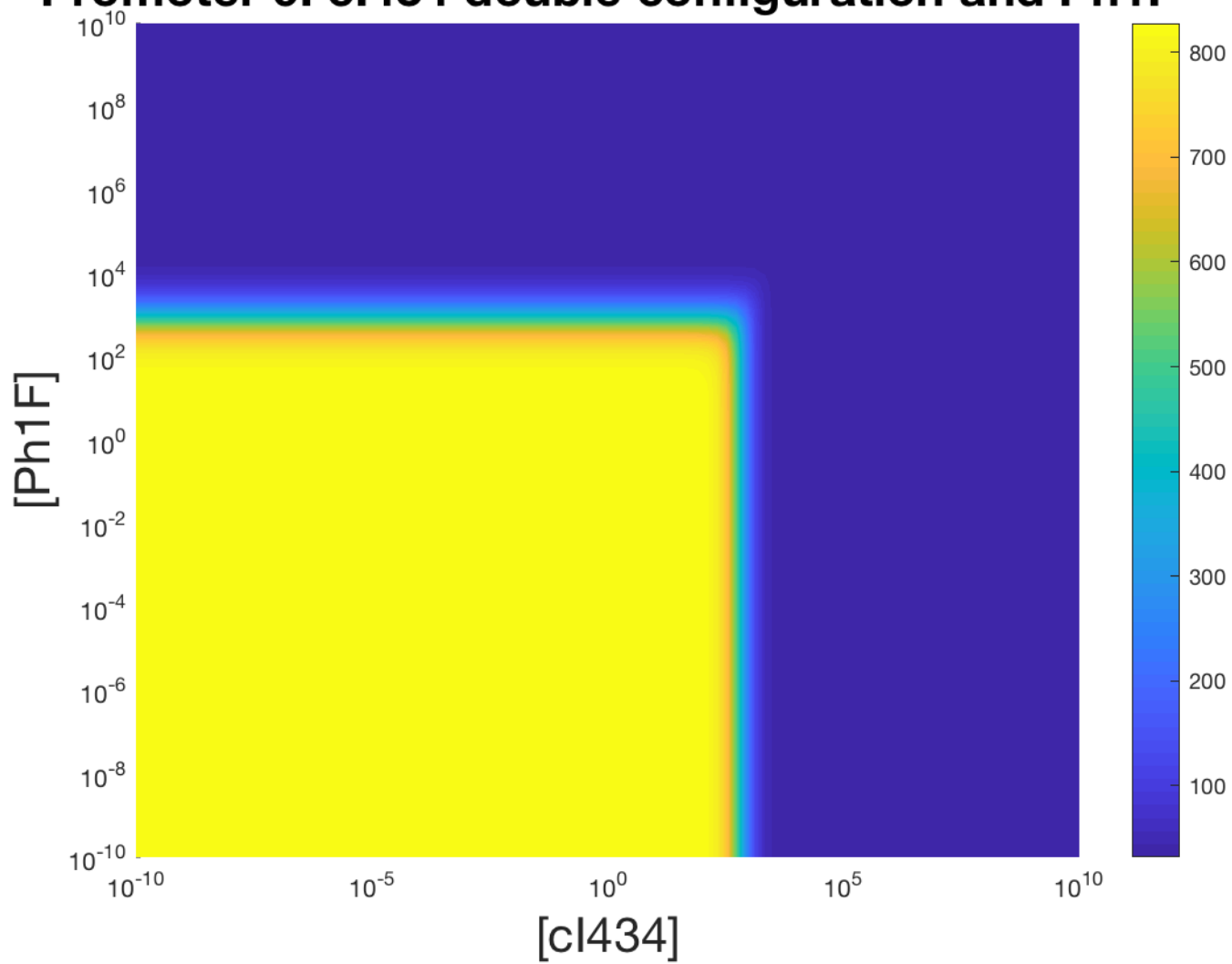


## Promoter 8: cl434 double configuration and CymR

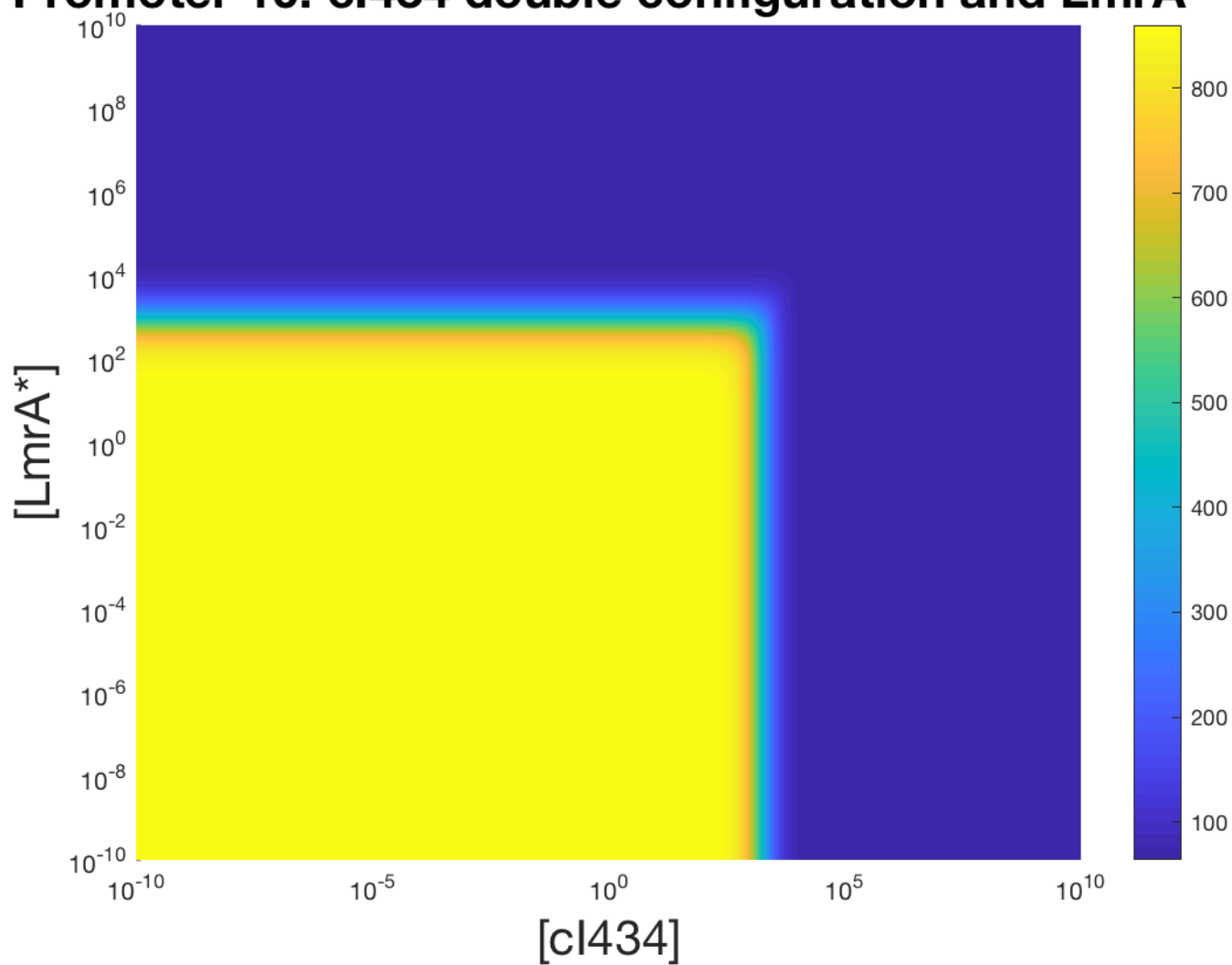




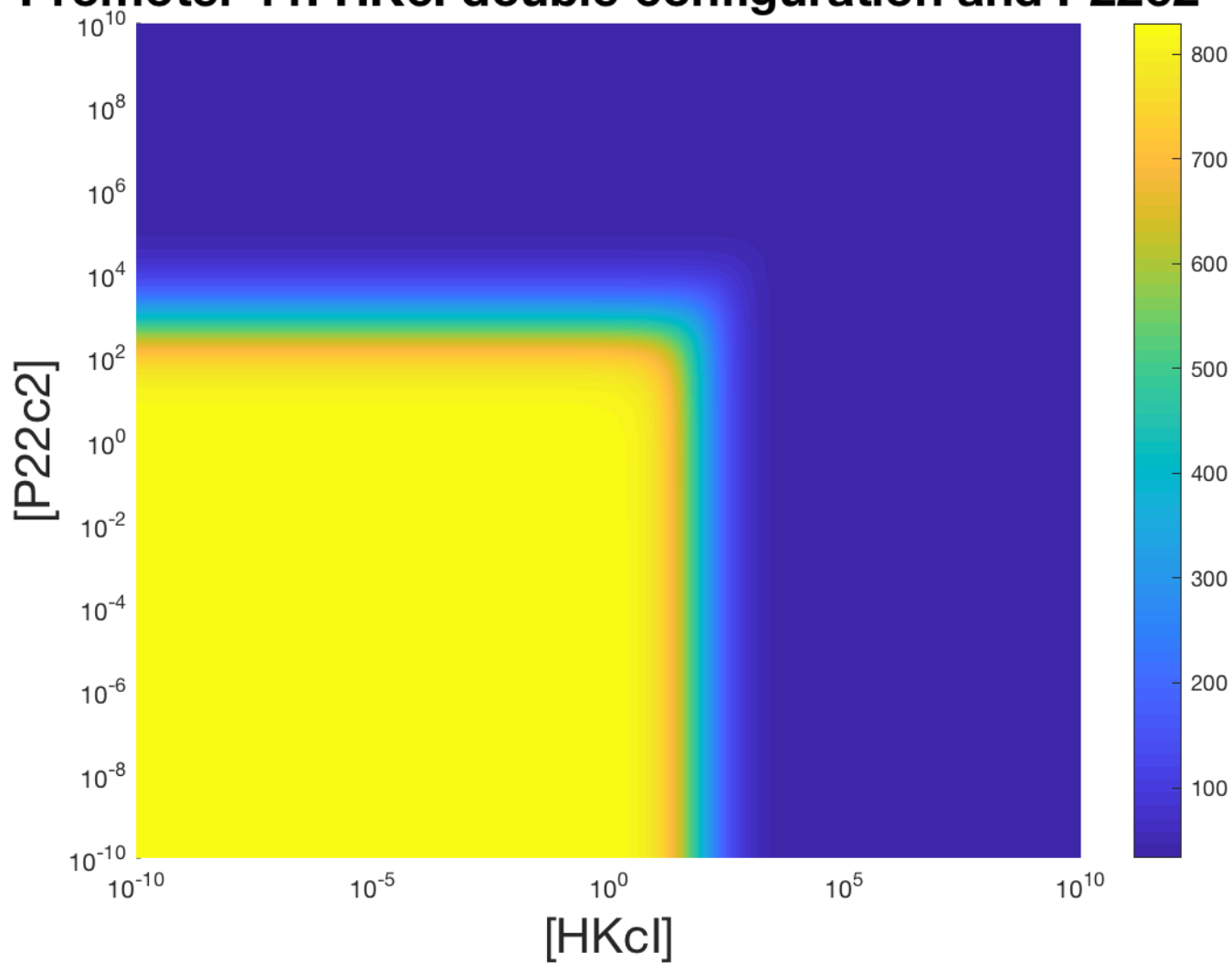
## Promoter 9: cl434 double configuration and Ph1F



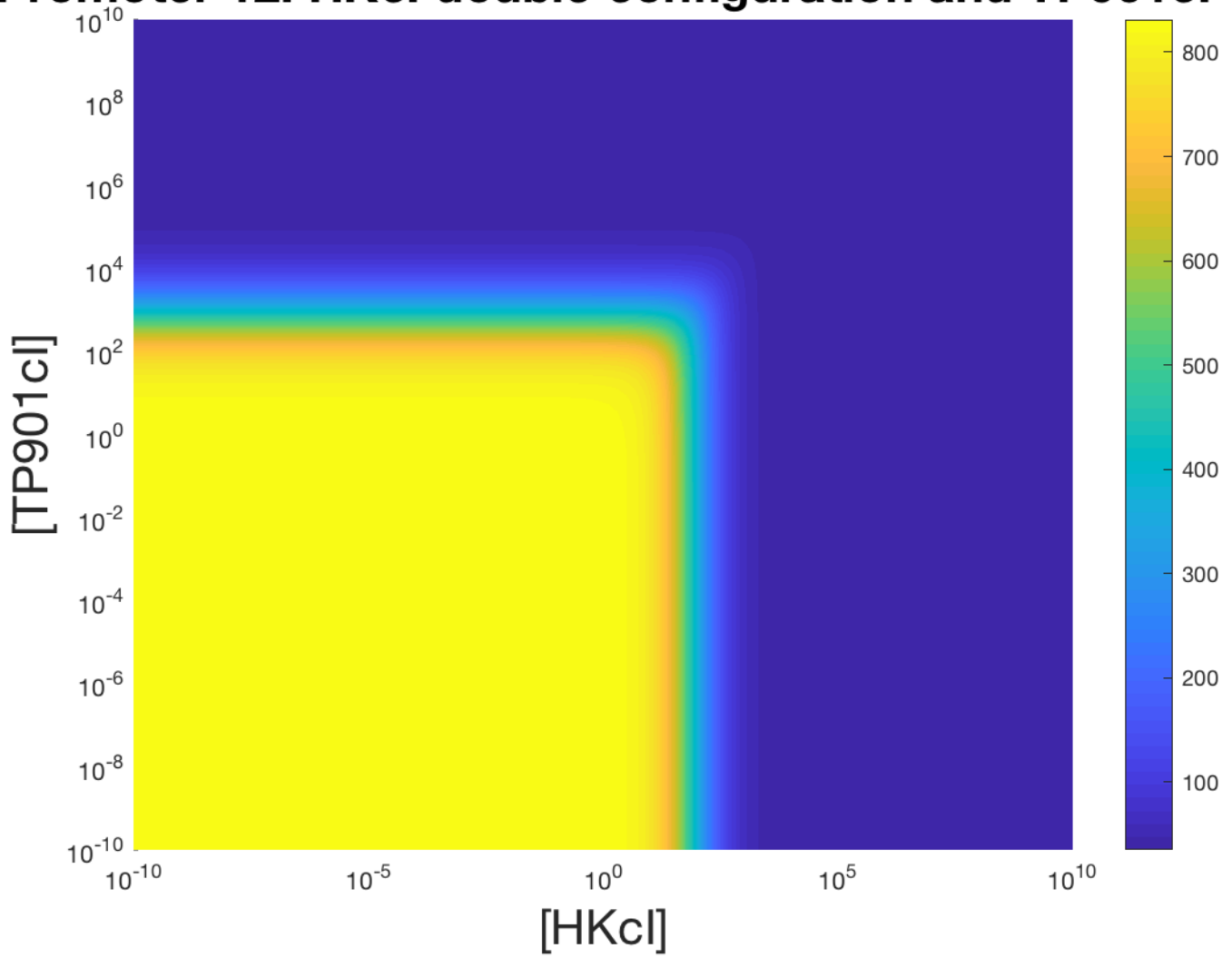
## Promoter 10: cl434 double configuration and LmrA\*



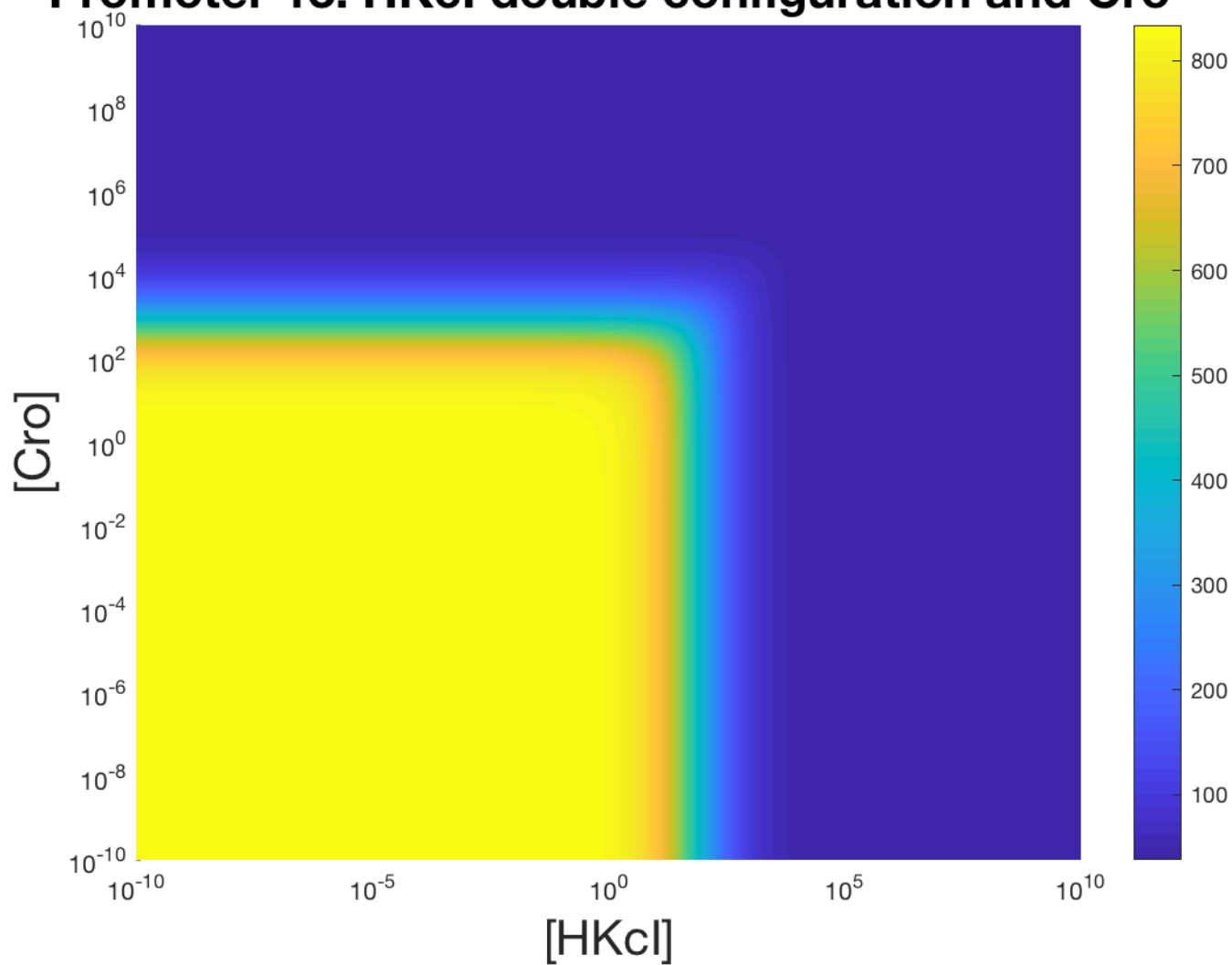
## Promoter 11: HKcl double configuration and P22c2



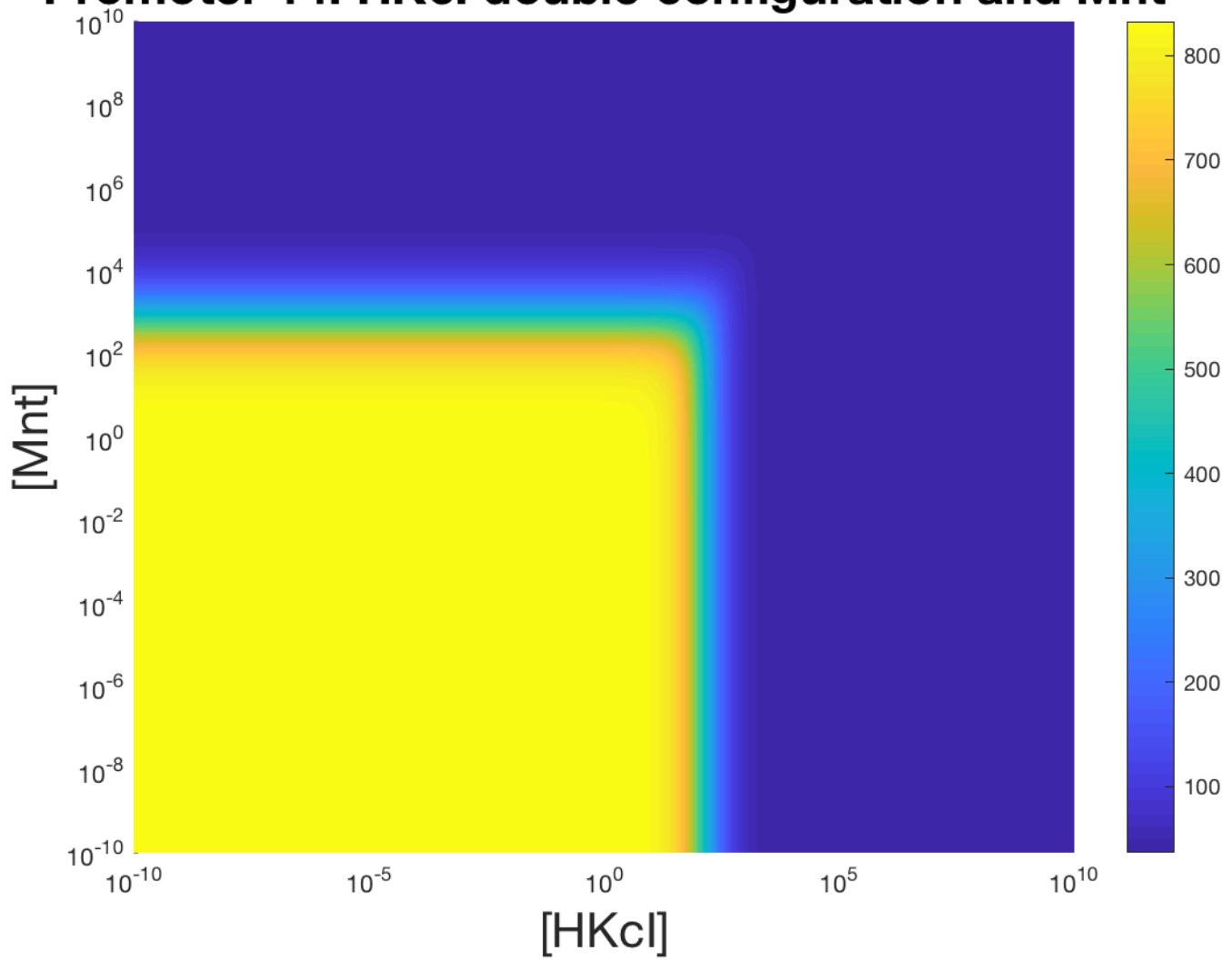
## Promoter 12: HKcl double configuration and TP901cl



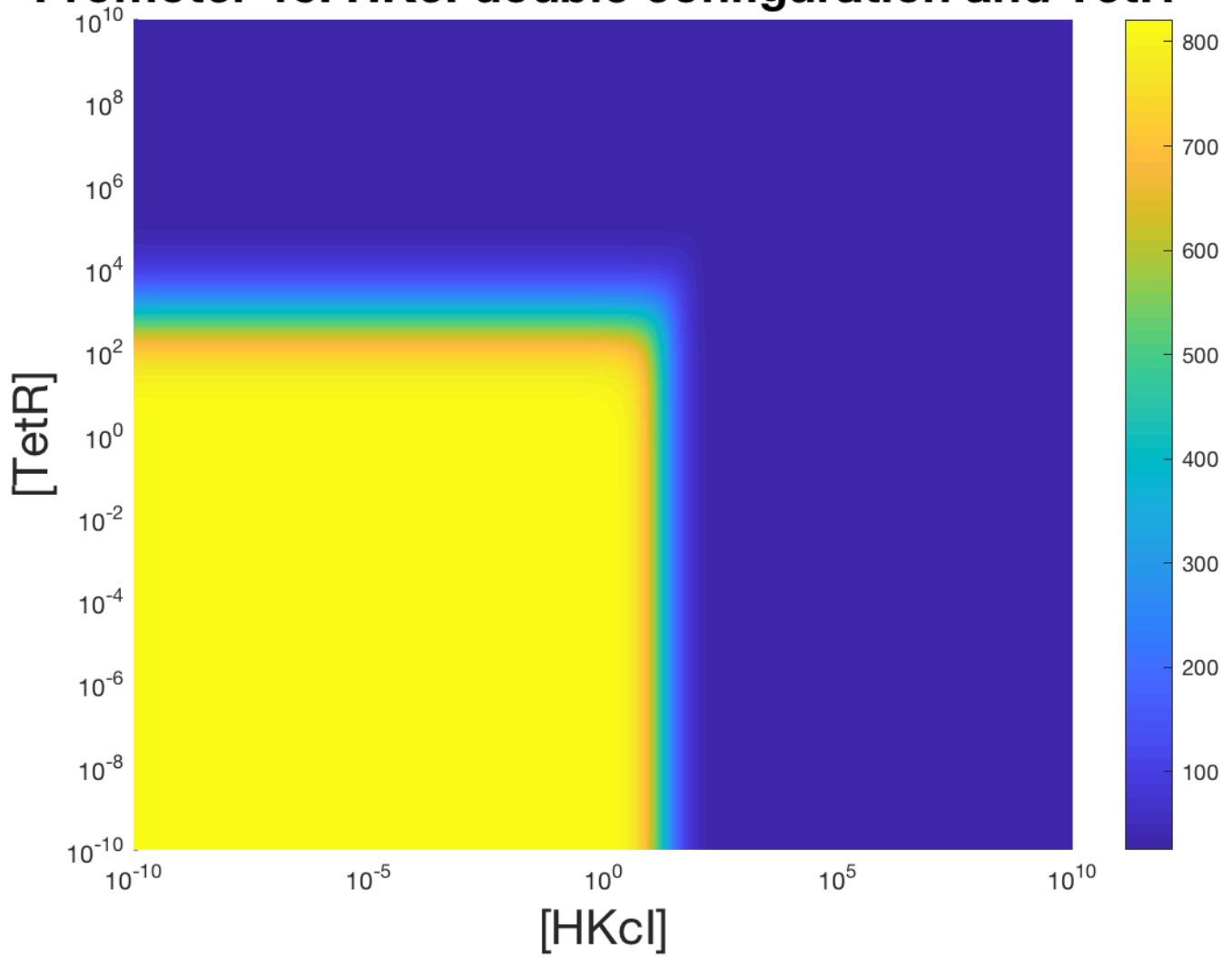
## Promoter 13: HKcl double configuration and Cro



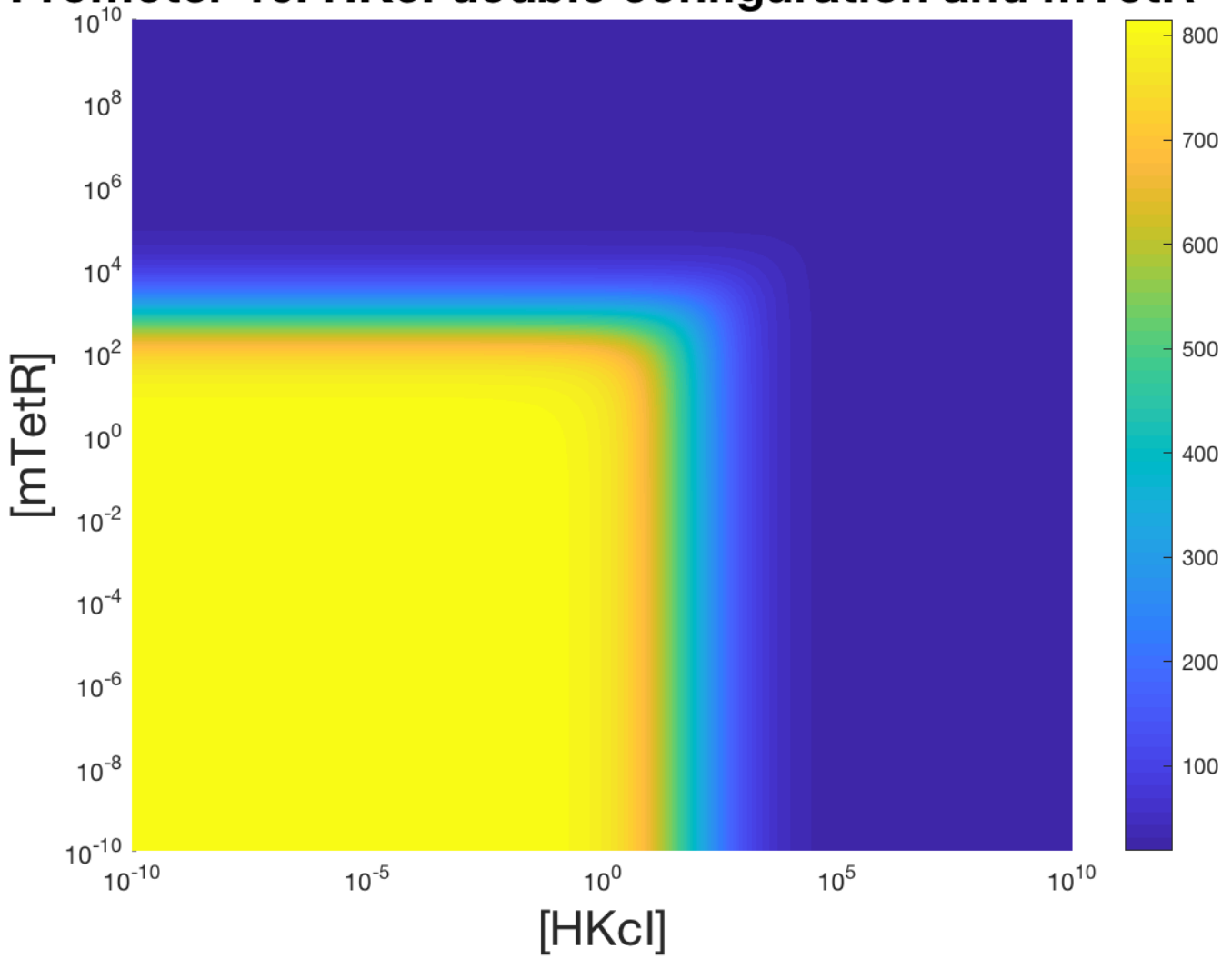
## Promoter 14: HKcl double configuration and Mnt



## Promoter 15: HKcl double configuration and TetR

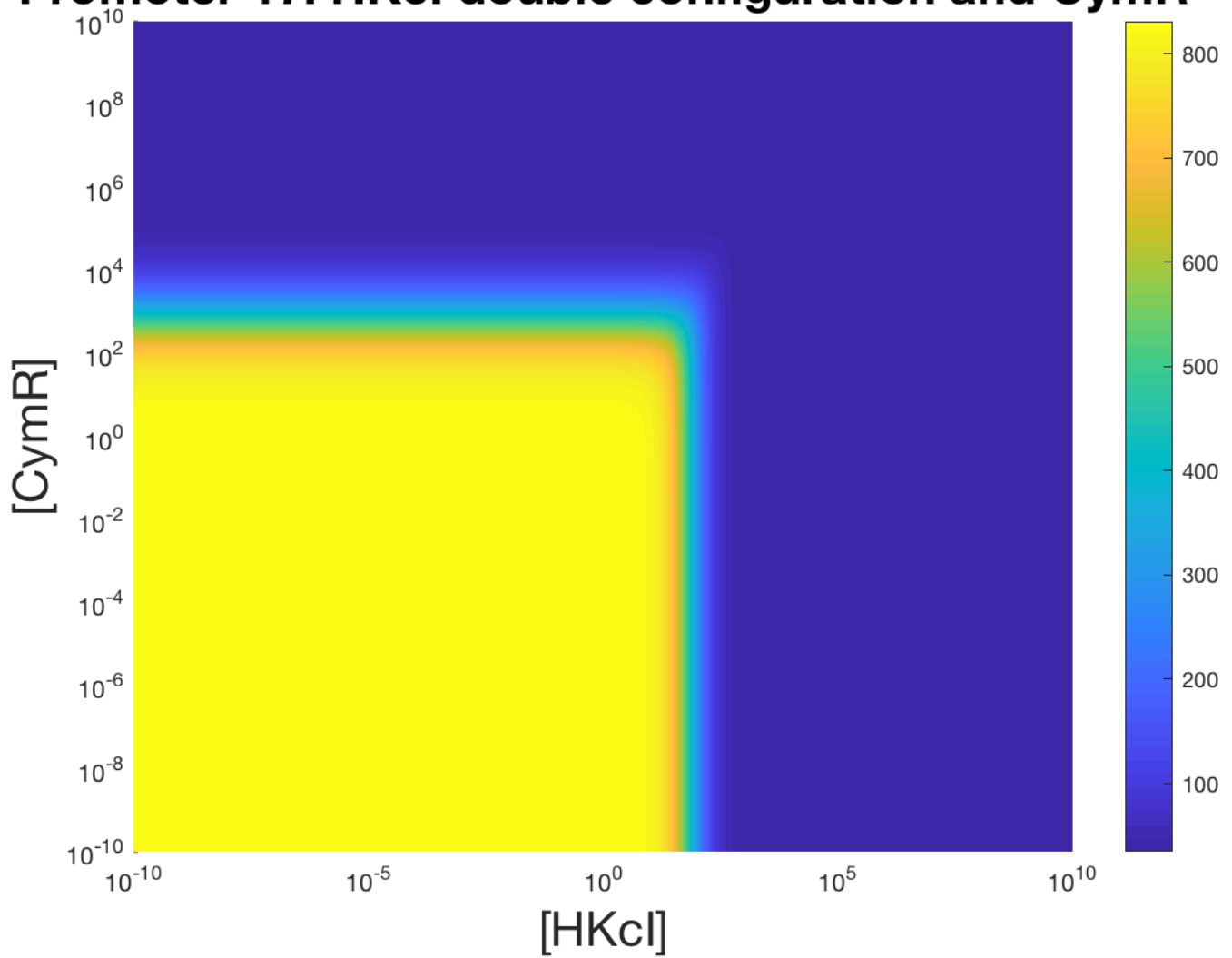


## Promoter 16: HKcl double configuration and mTetR

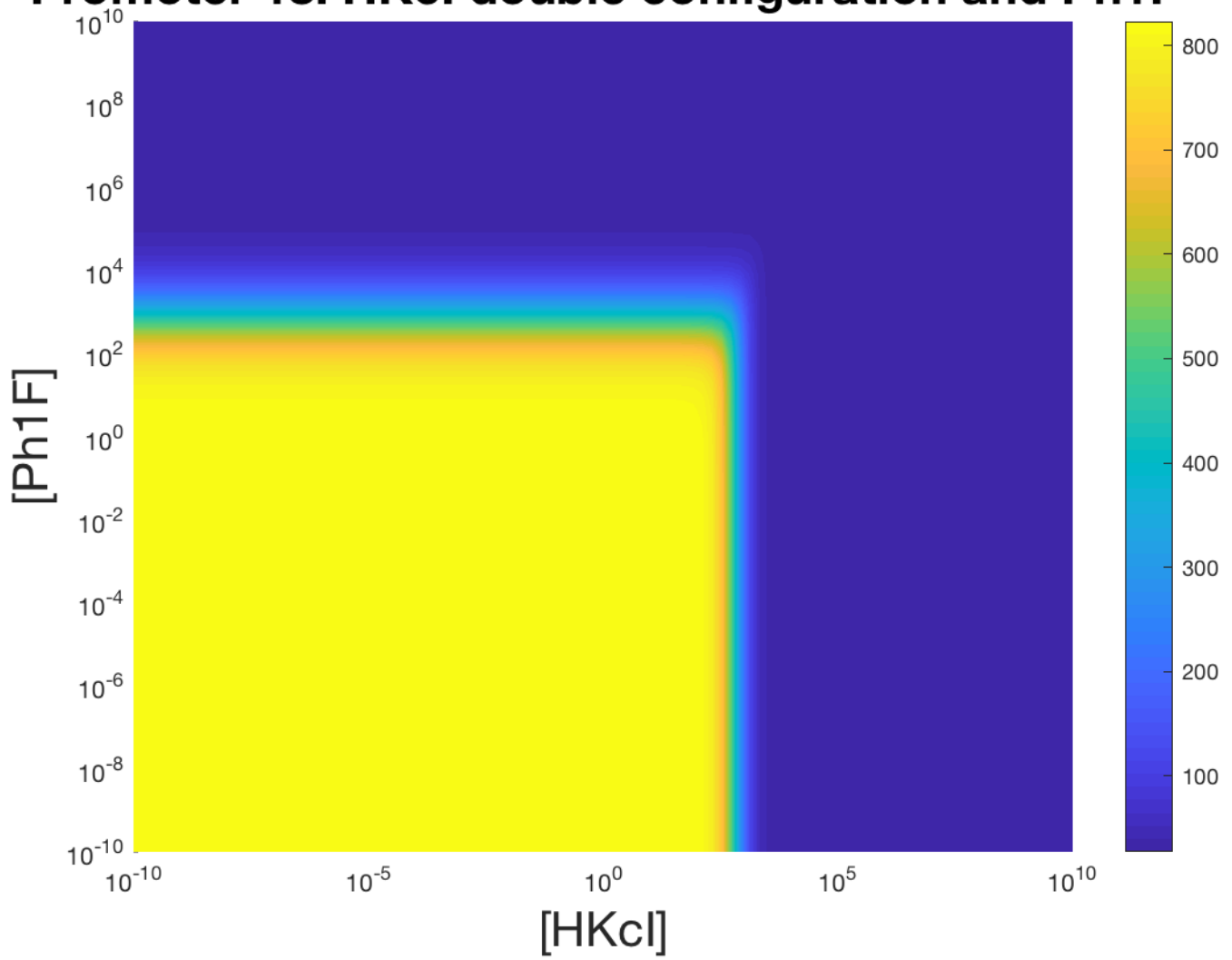




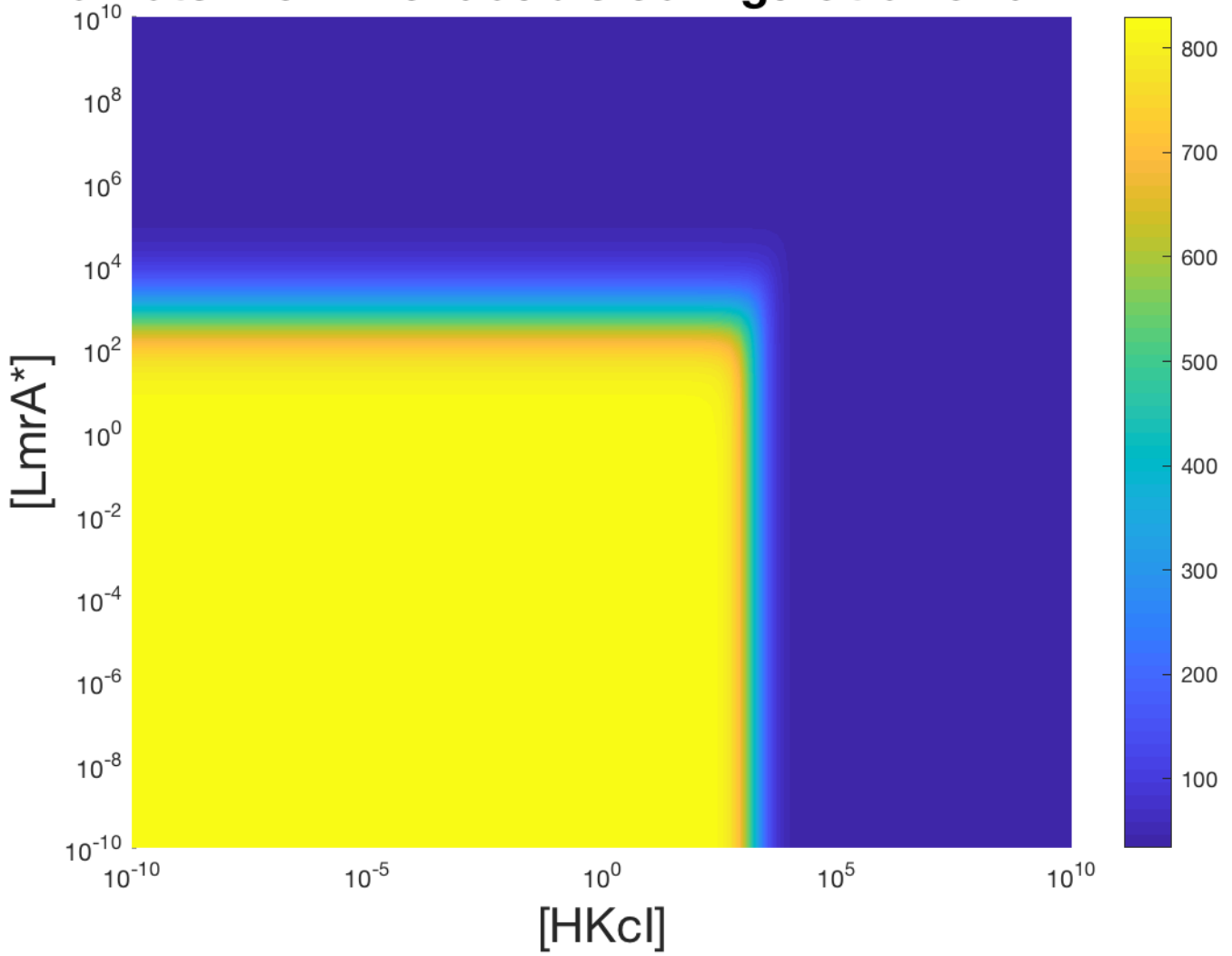
## Promoter 17: HKcl double configuration and CymR



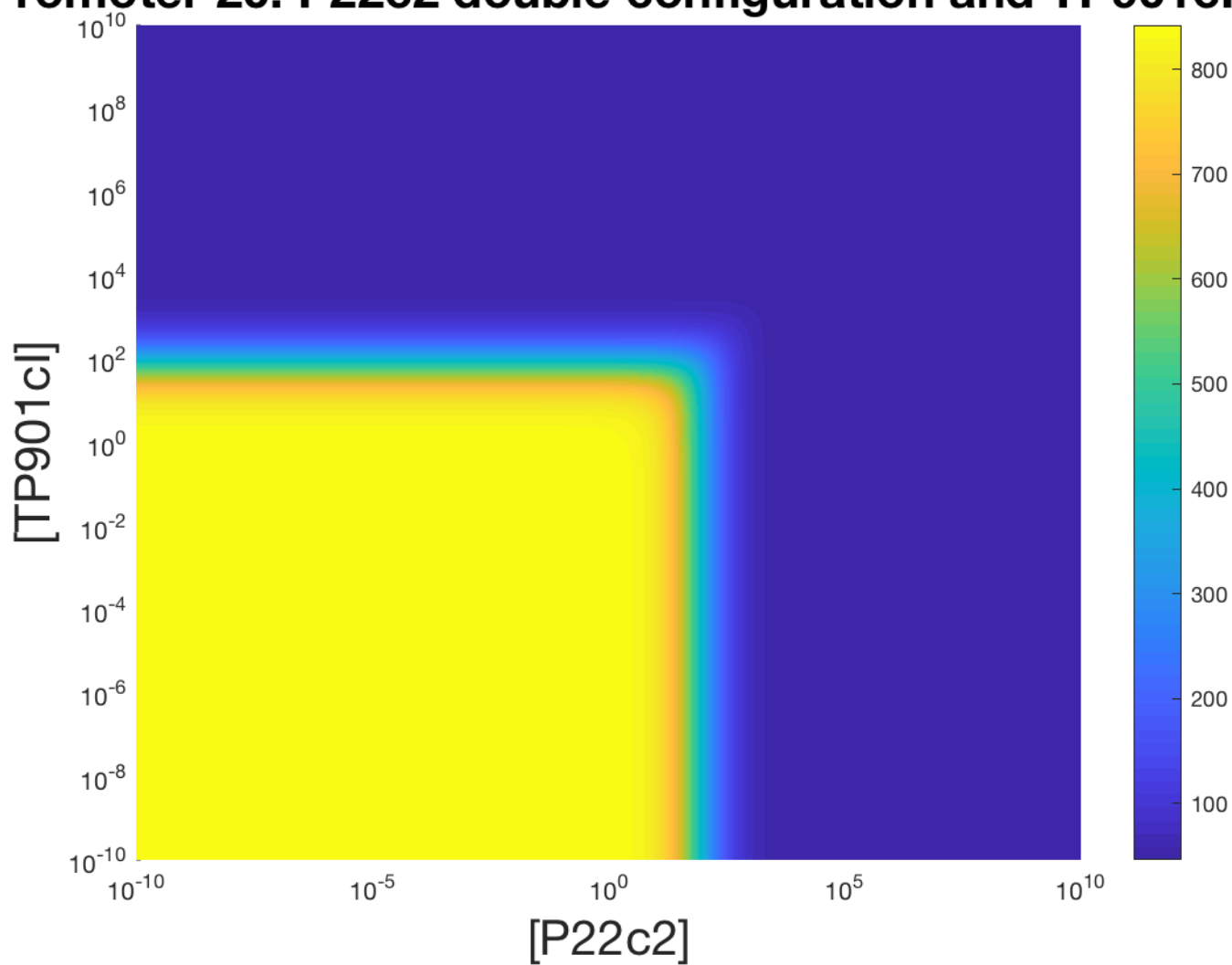
## Promoter 18: HKcl double configuration and Ph1F



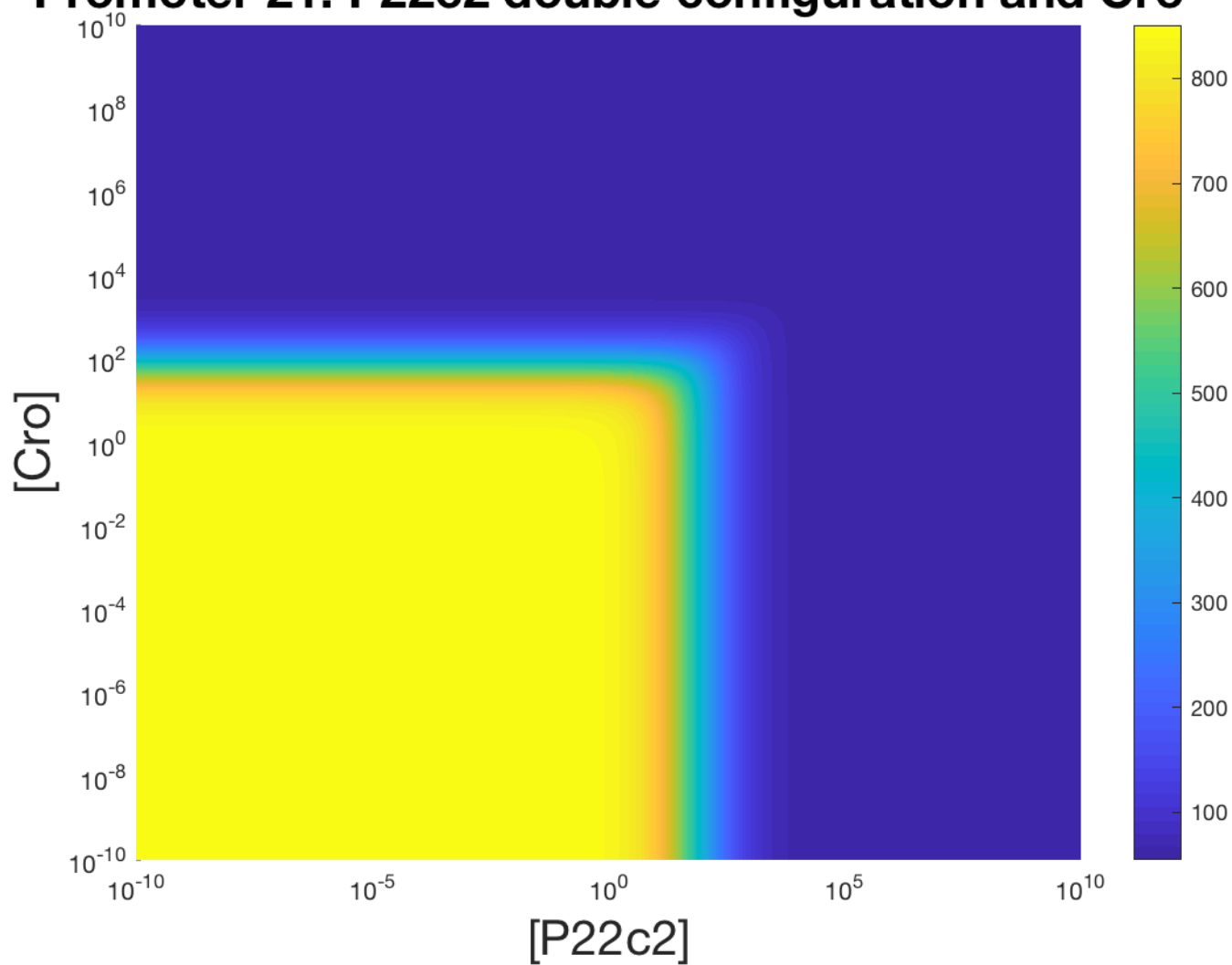
# Promoter 19: HKcl double configuration and LmrA\*



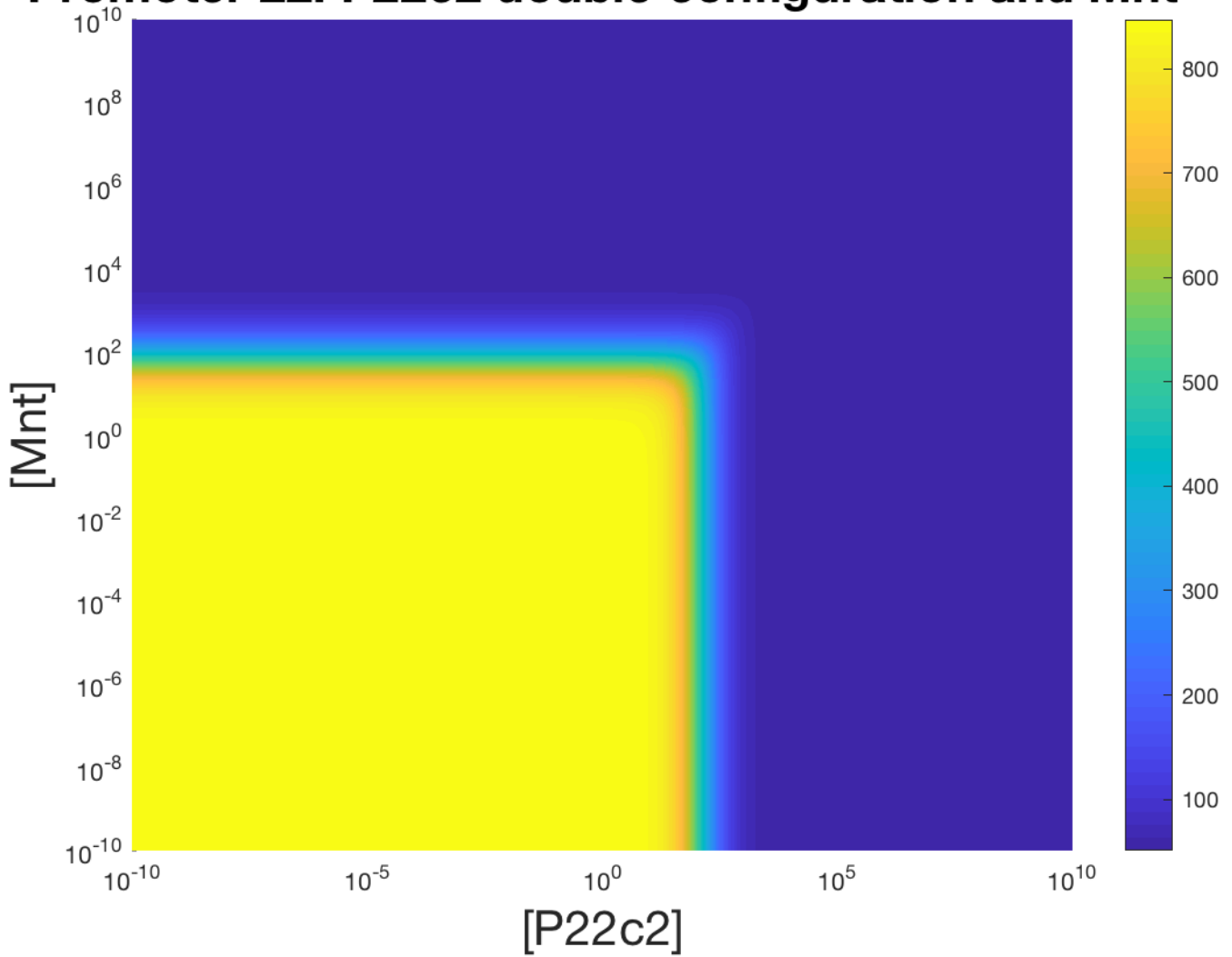
## Promoter 20: P22c2 double configuration and TP901cl



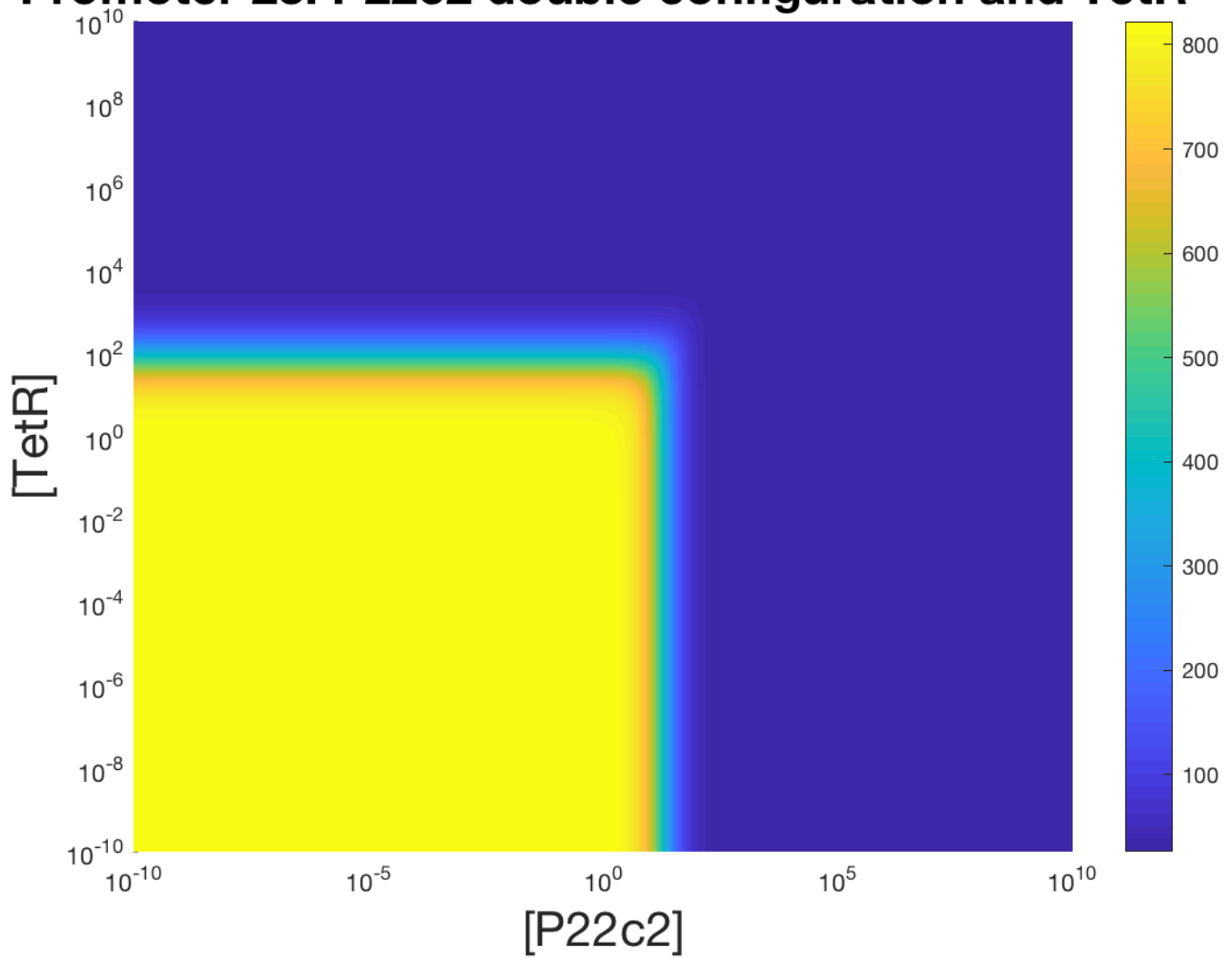
## Promoter 21: P22c2 double configuration and Cro



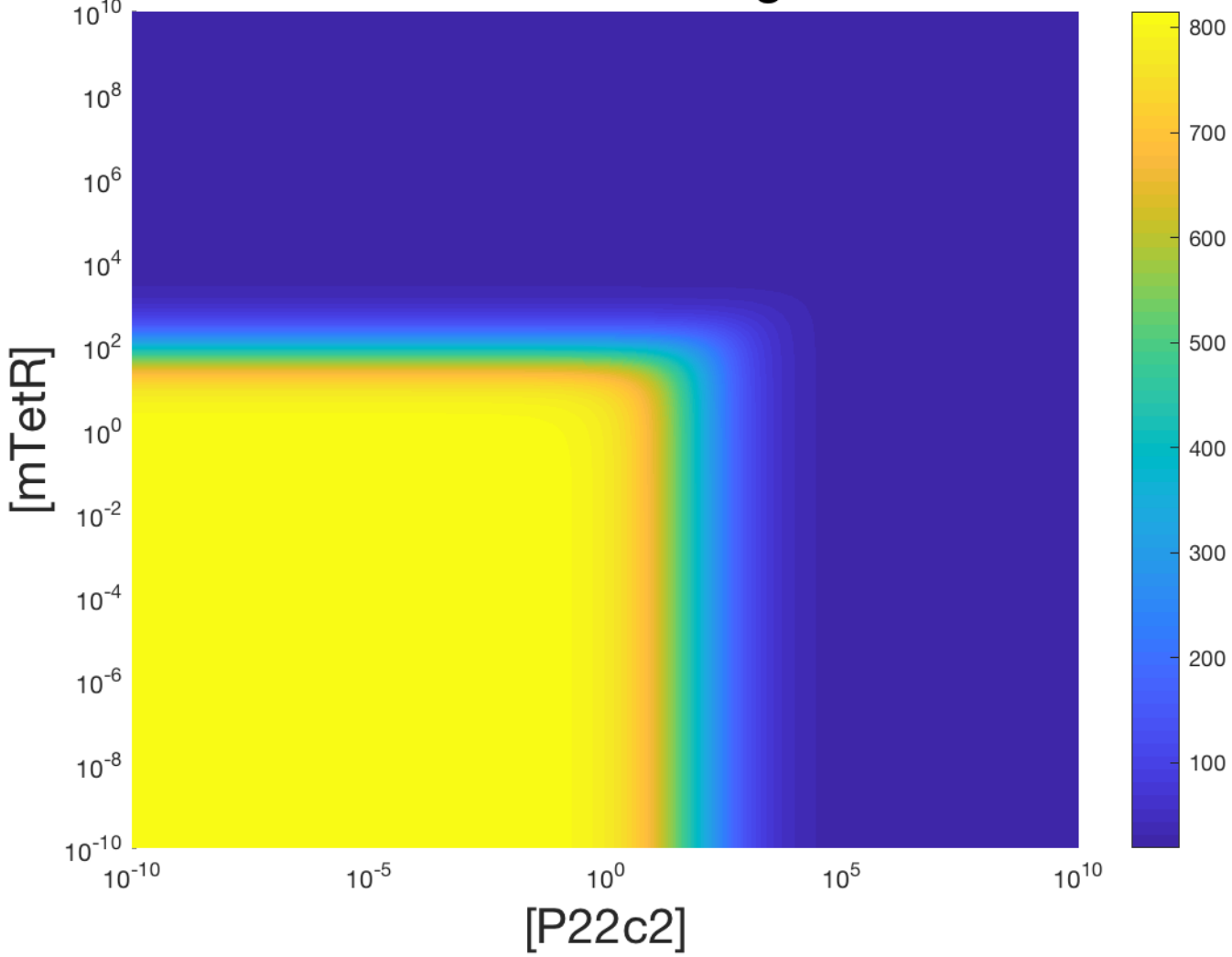
## Promoter 22: P22c2 double configuration and Mnt



## Promoter 23: P22c2 double configuration and TetR

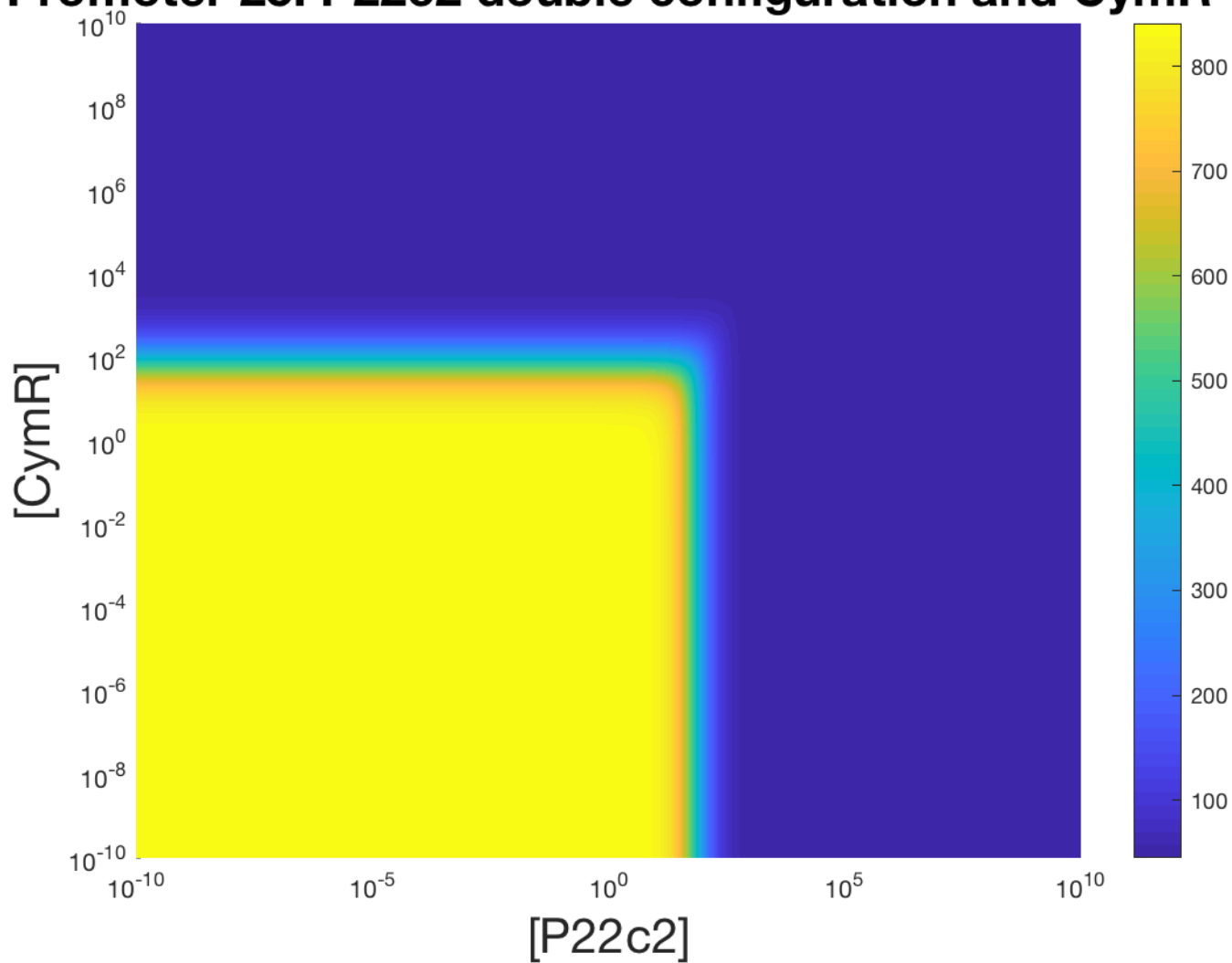


# Promoter 24: P22c2 double configuration and mTetR

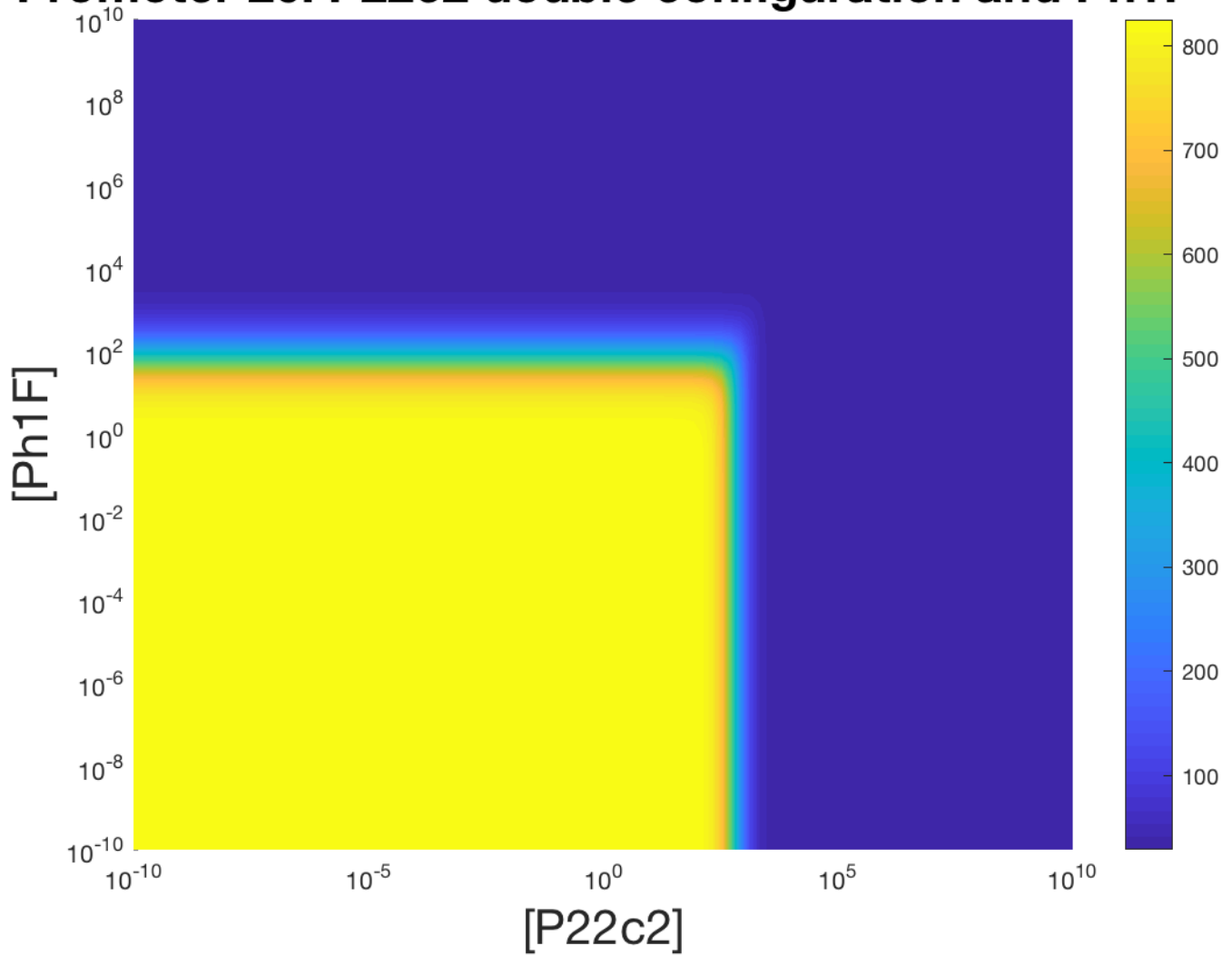




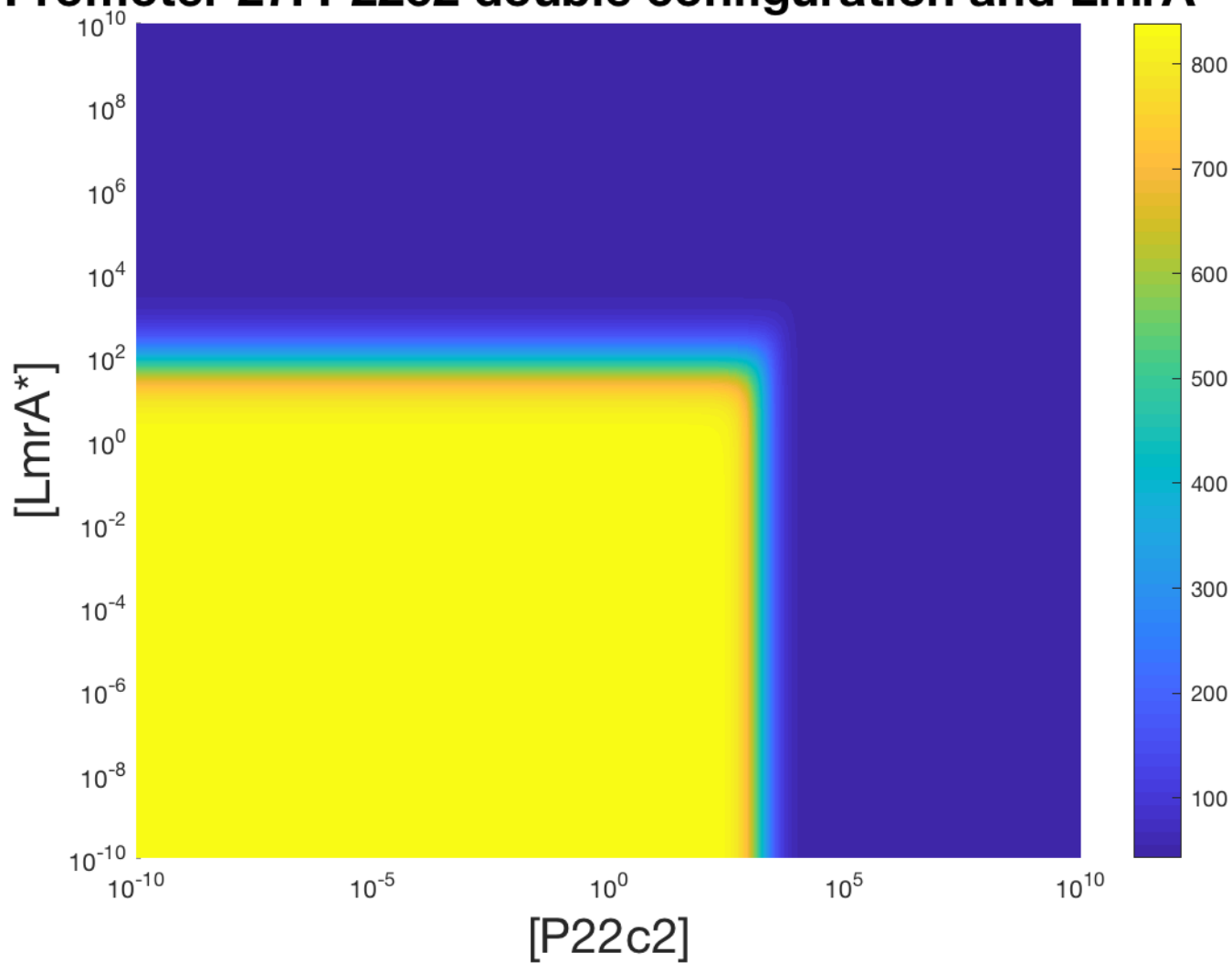
## Promoter 25: P22c2 double configuration and CymR



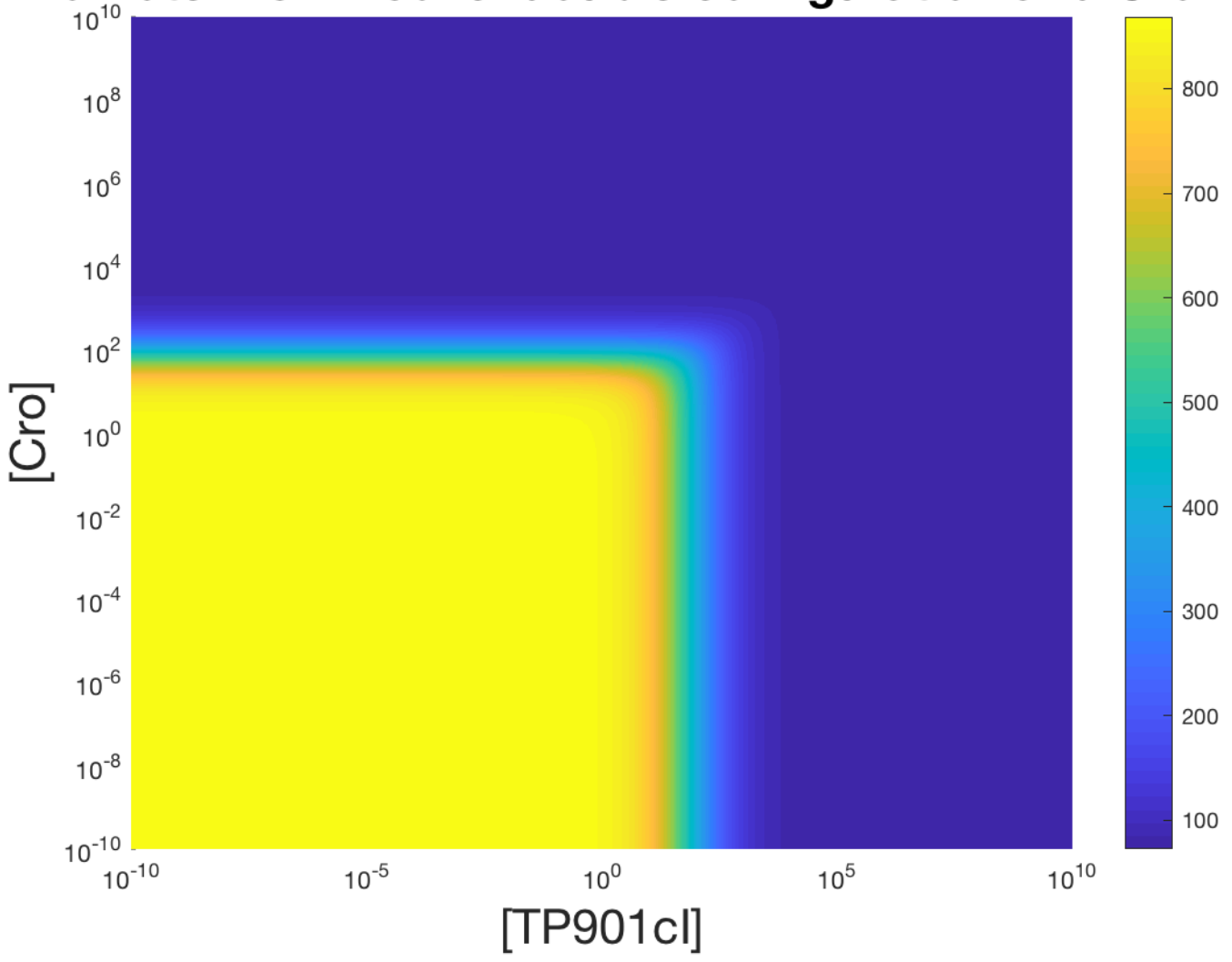
## Promoter 26: P22c2 double configuration and Ph1F



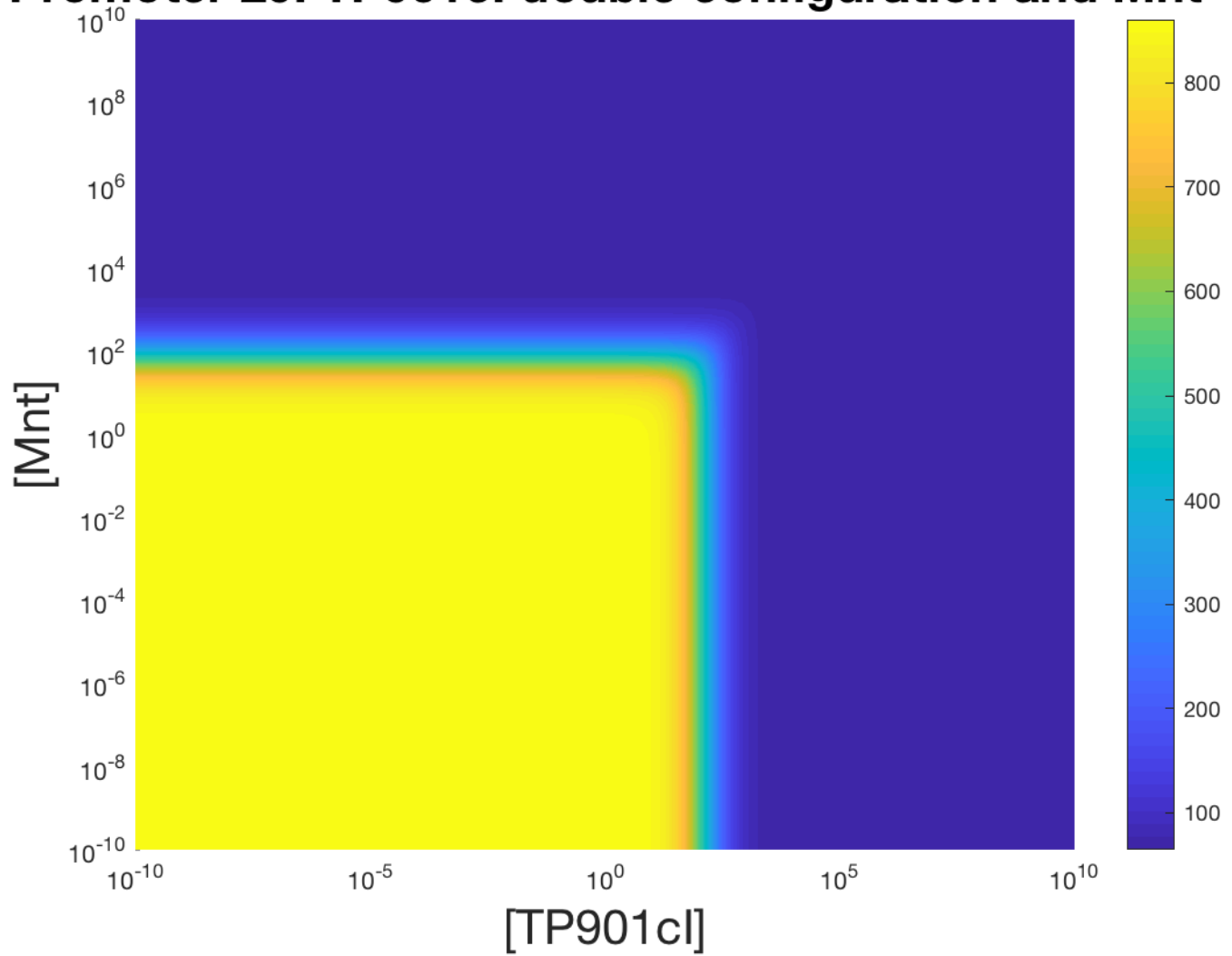
## Promoter 27: P22c2 double configuration and LmrA\*



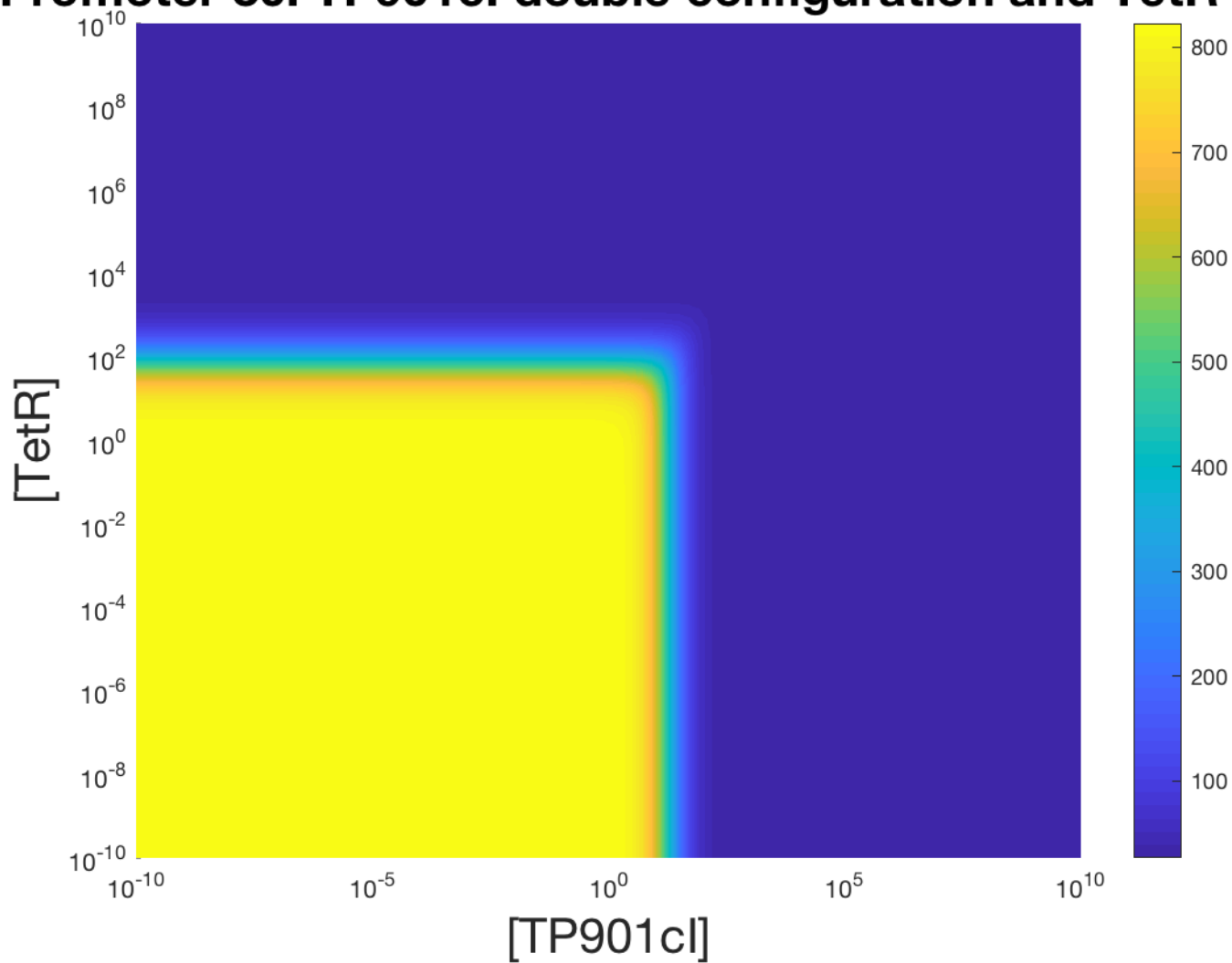
# Promoter 28: TP901cl double configuration and Cro



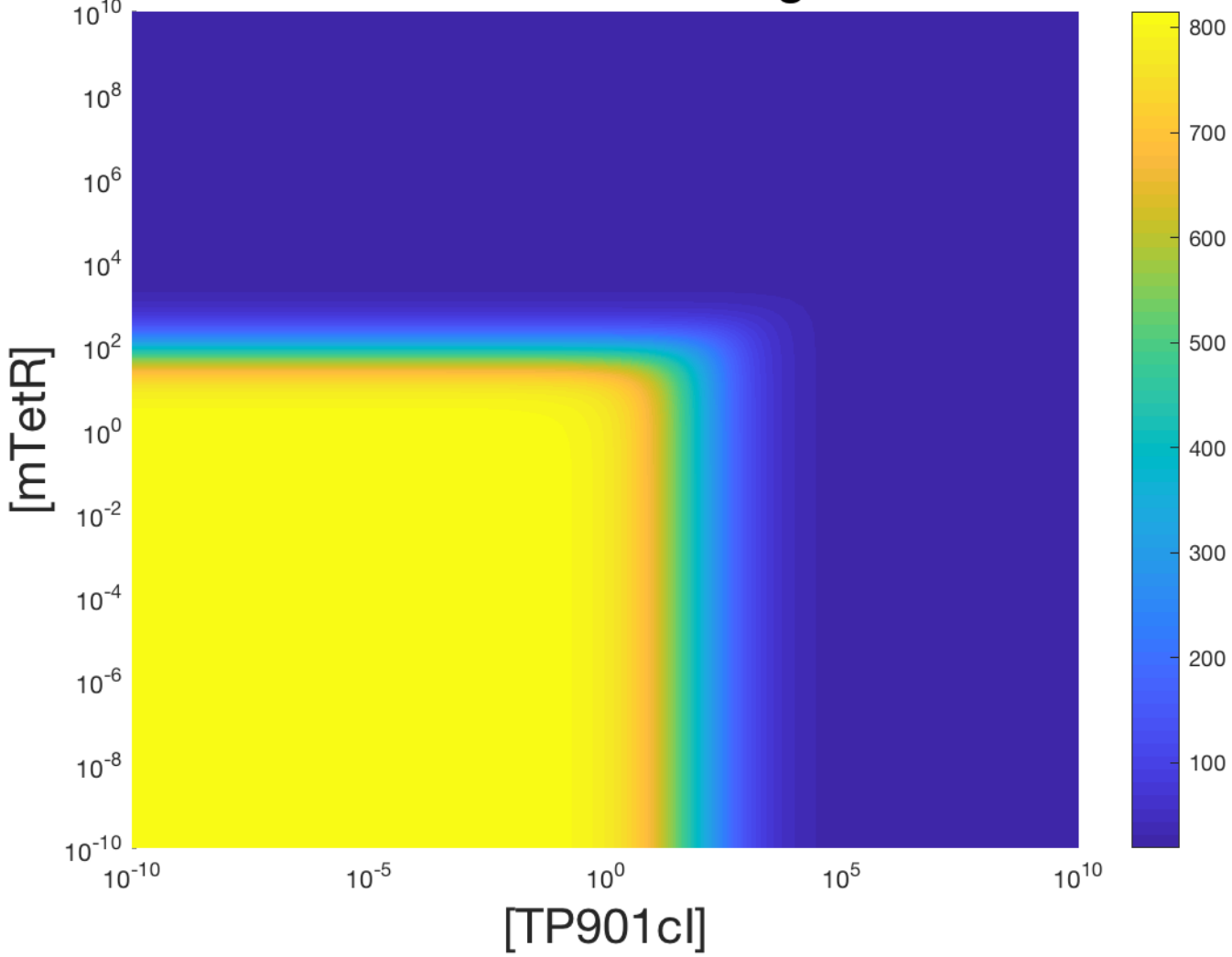
## Promoter 29: TP901cl double configuration and Mnt



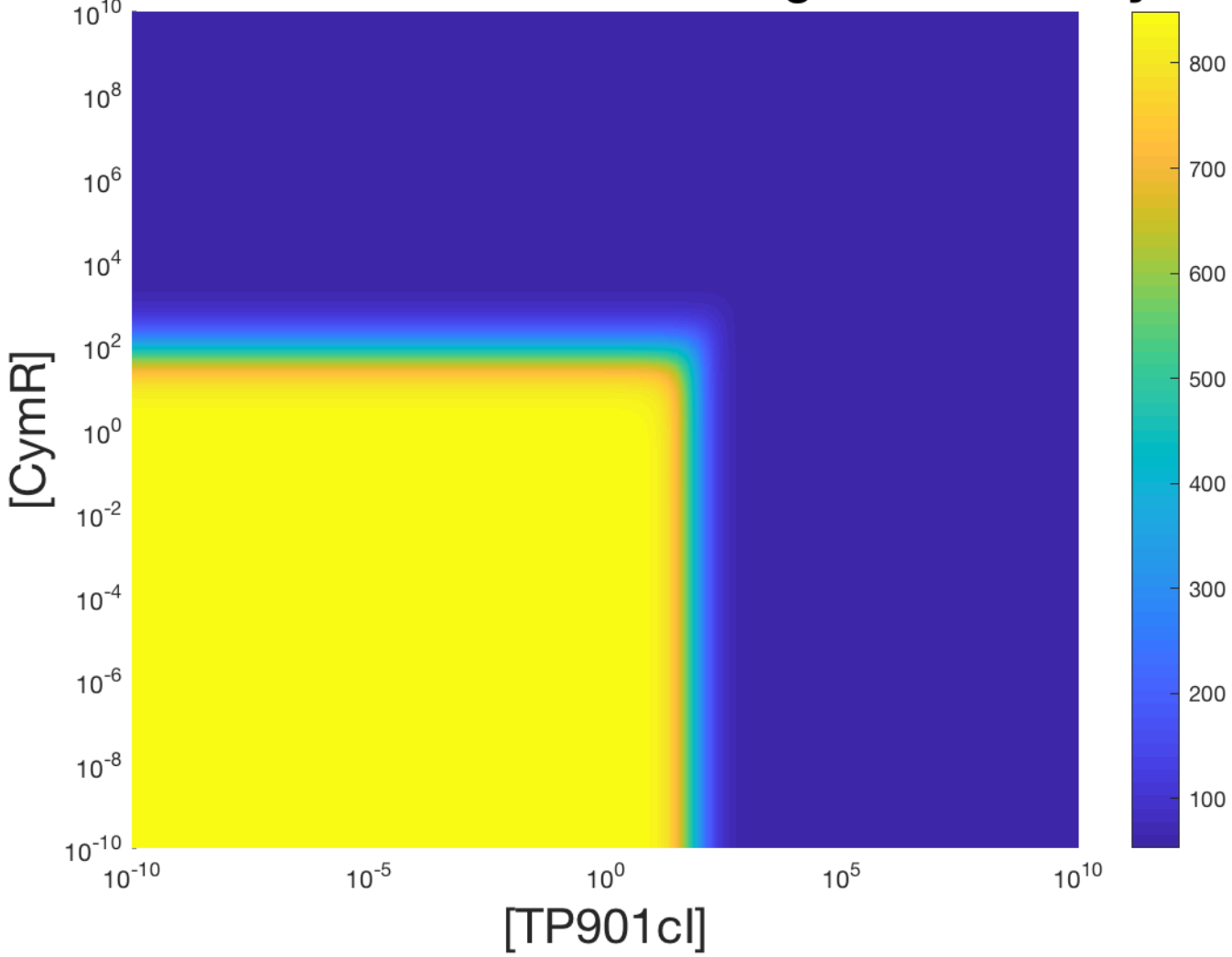
## Promoter 30: TP901cl double configuration and TetR



# Promoter 31: TP901cl double configuration and mTetR

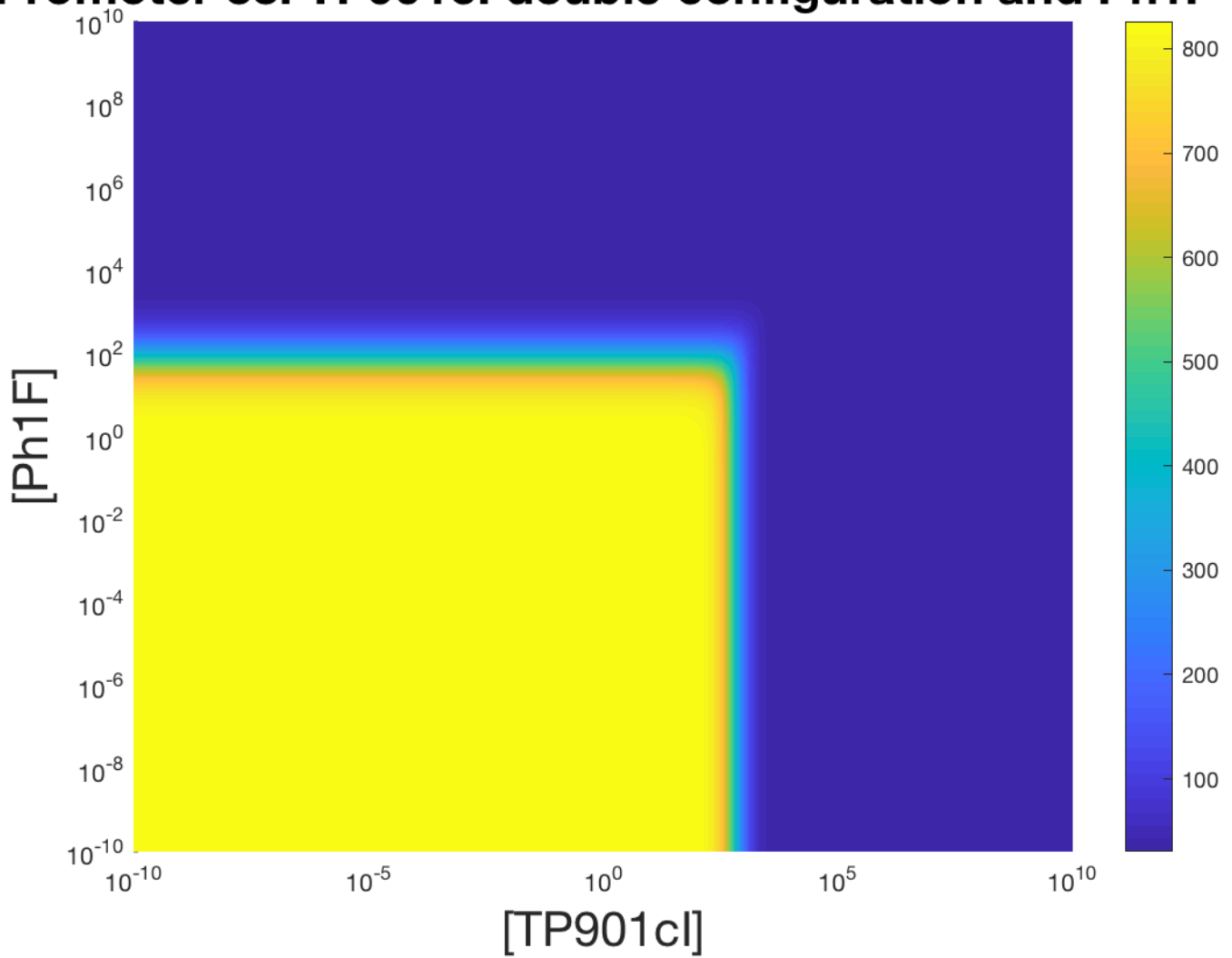


# Promoter 32: TP901cl double configuration and CymR

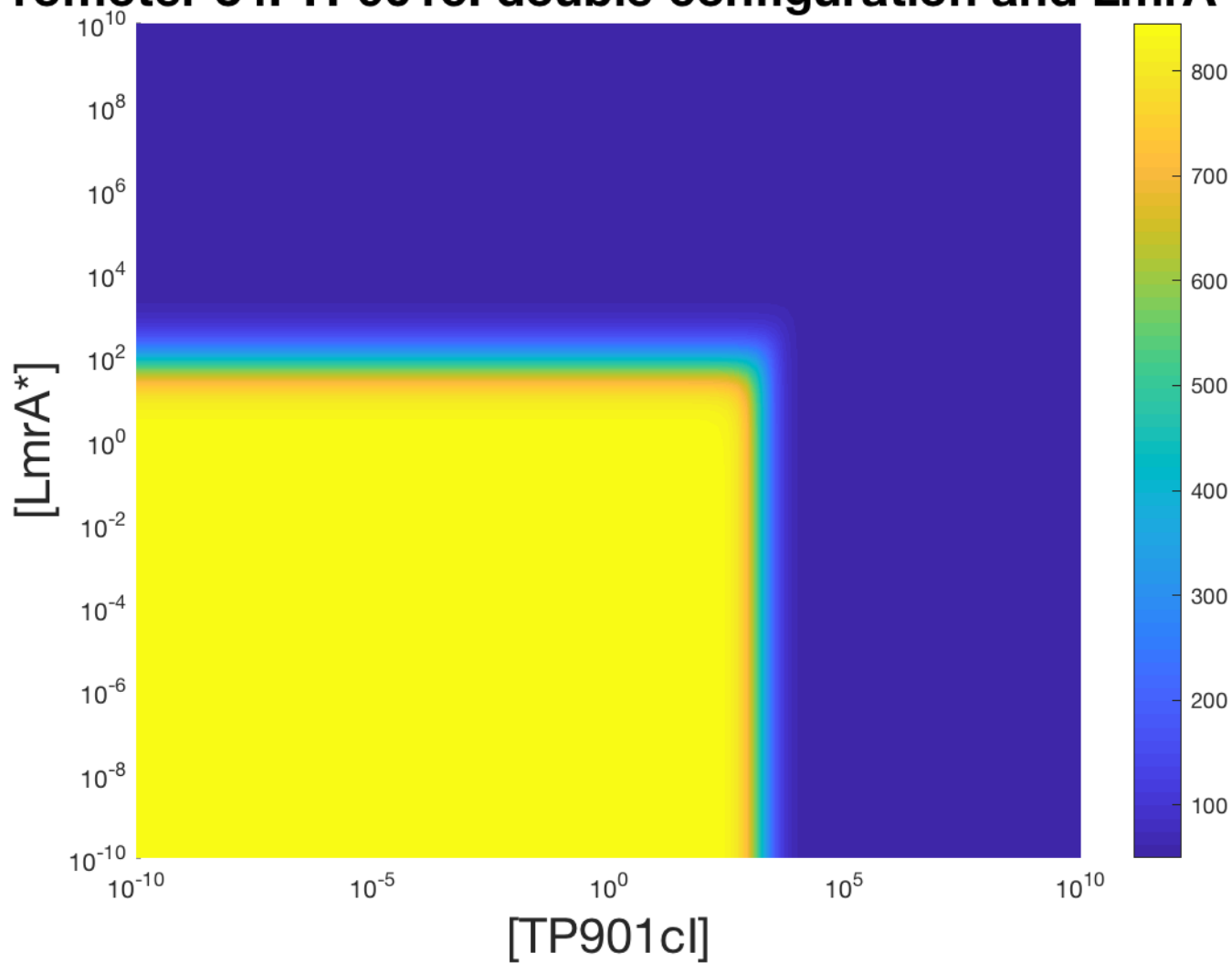




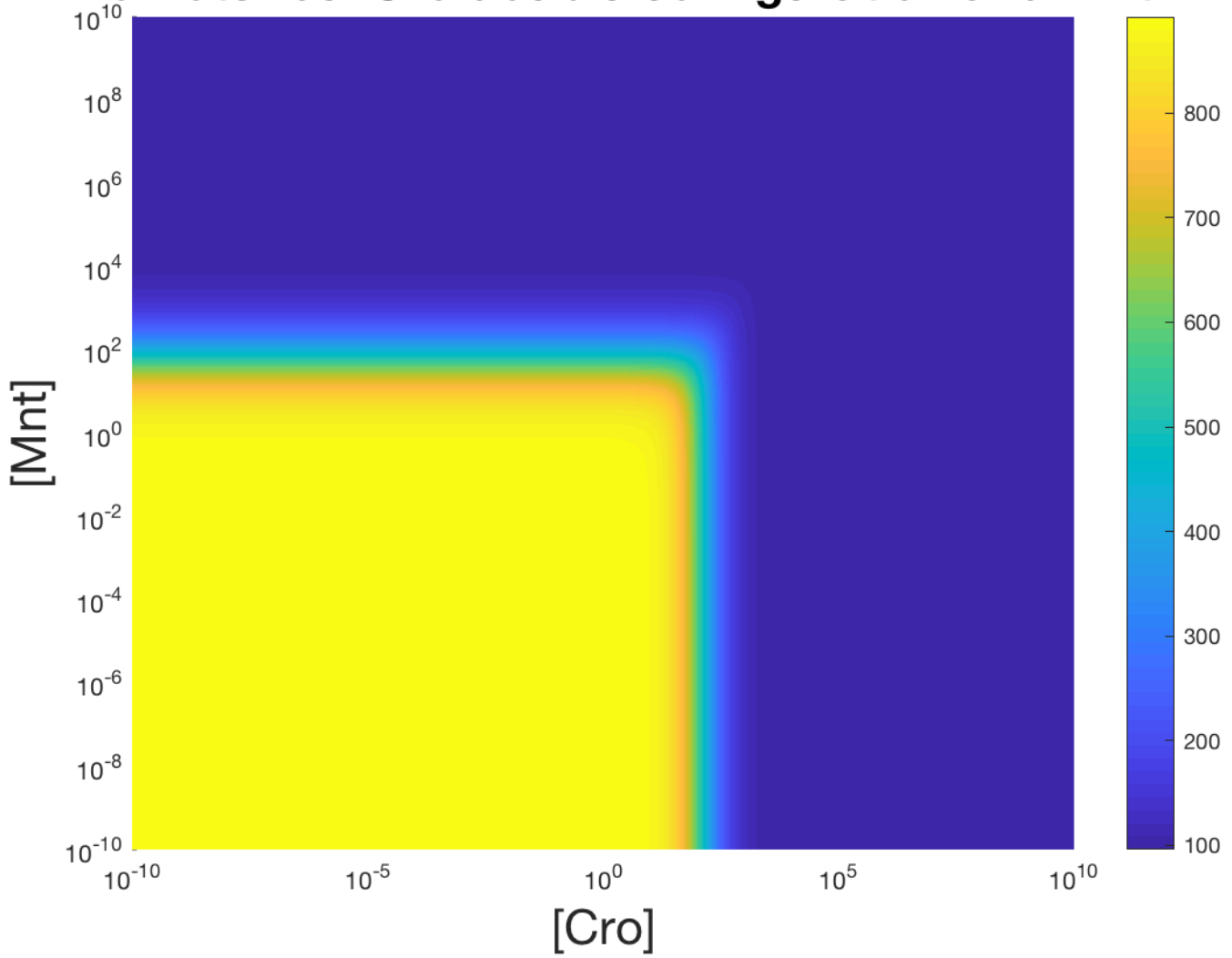
## Promoter 33: TP901cl double configuration and Ph1F



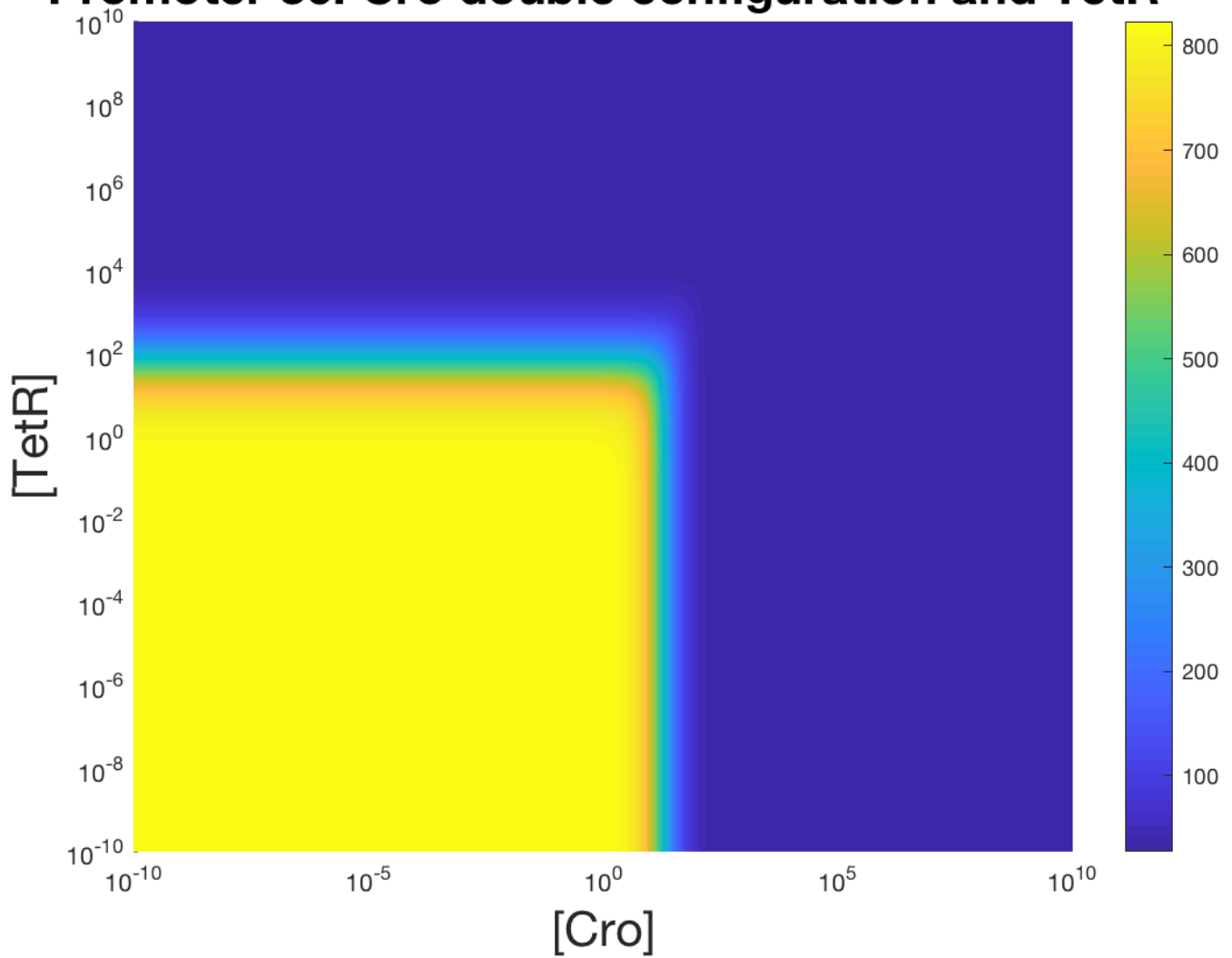
## Promoter 34: TP901cl double configuration and LmrA\*



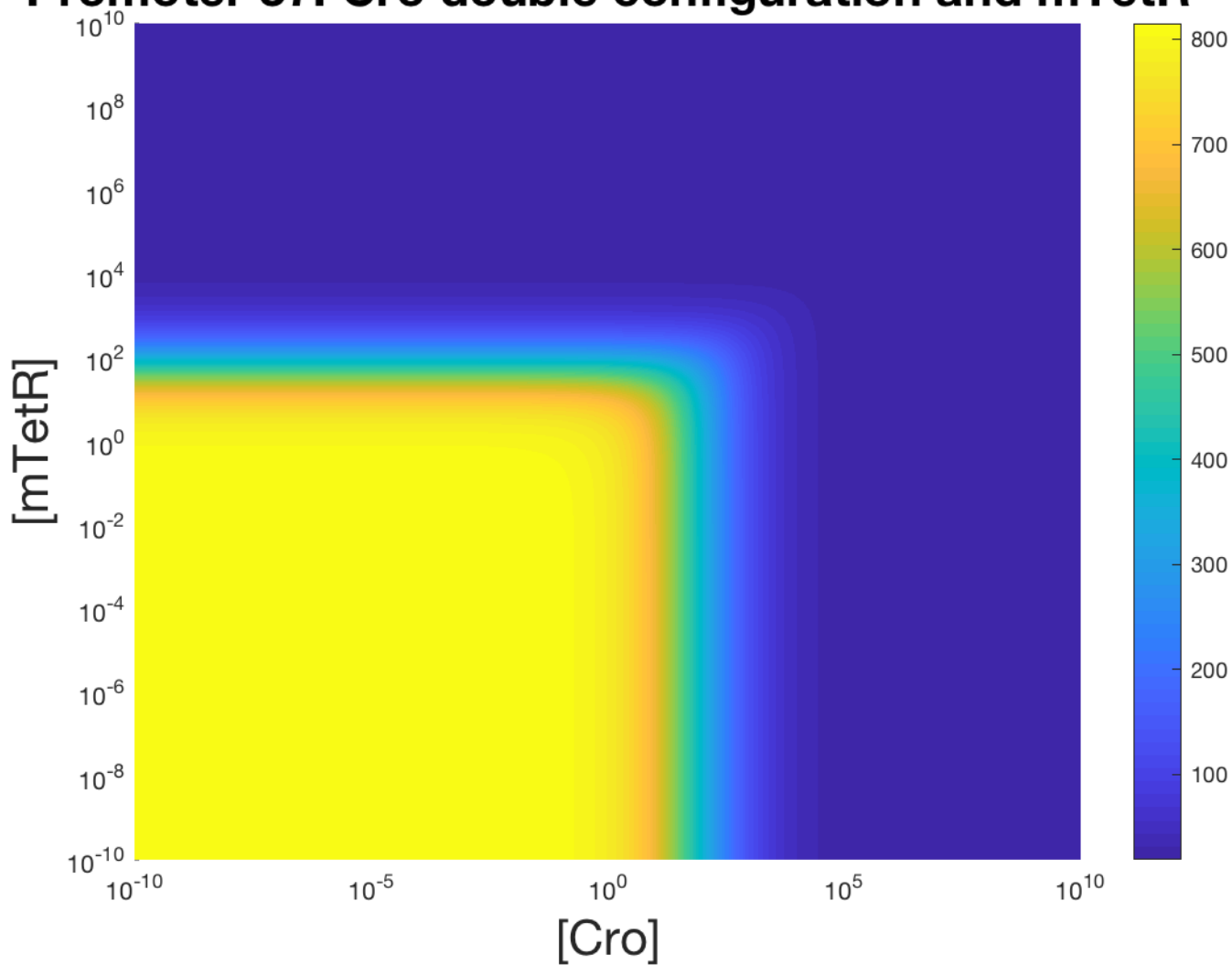
# Promoter 35: Cro double configuration and Mnt



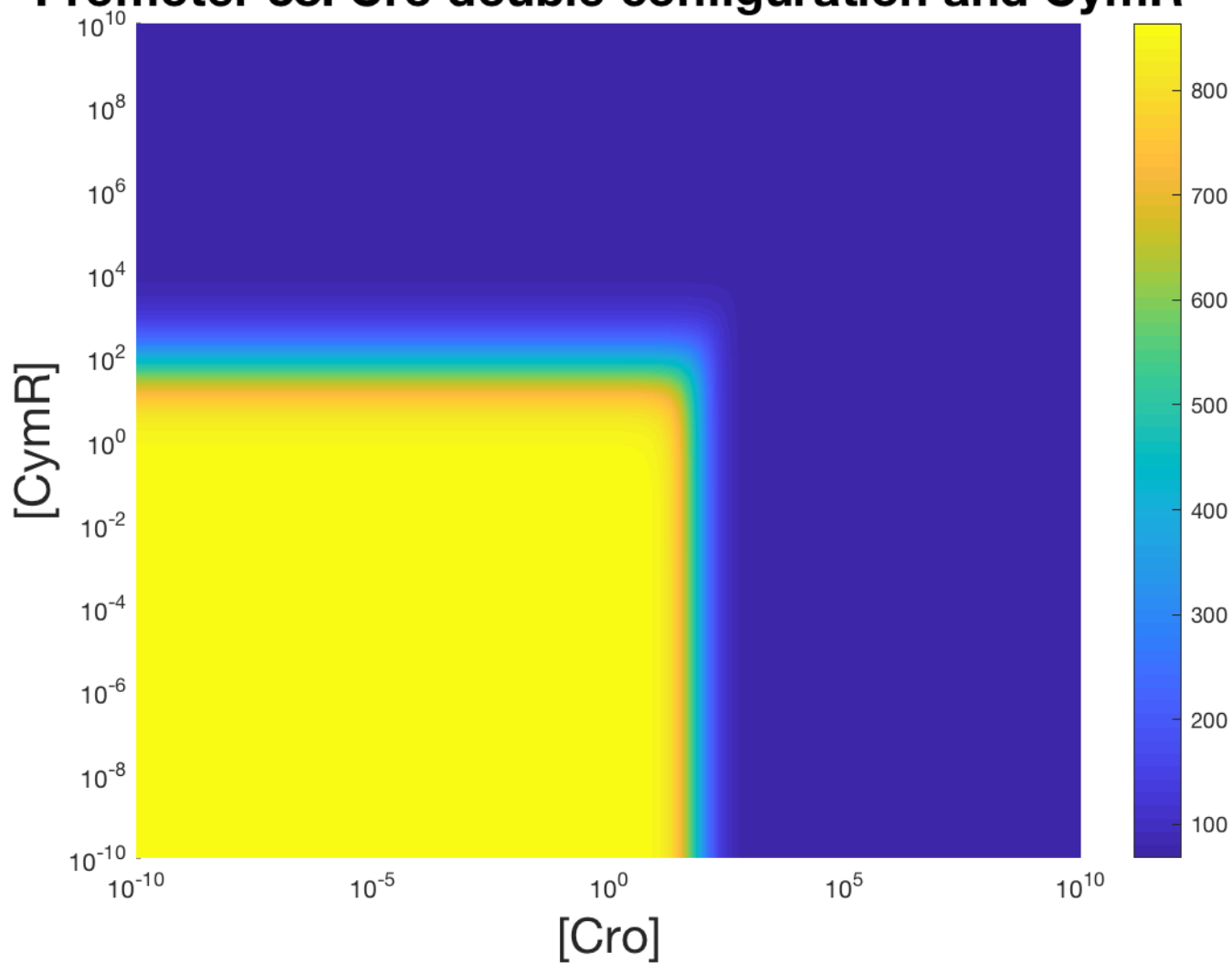
## Promoter 36: Cro double configuration and TetR



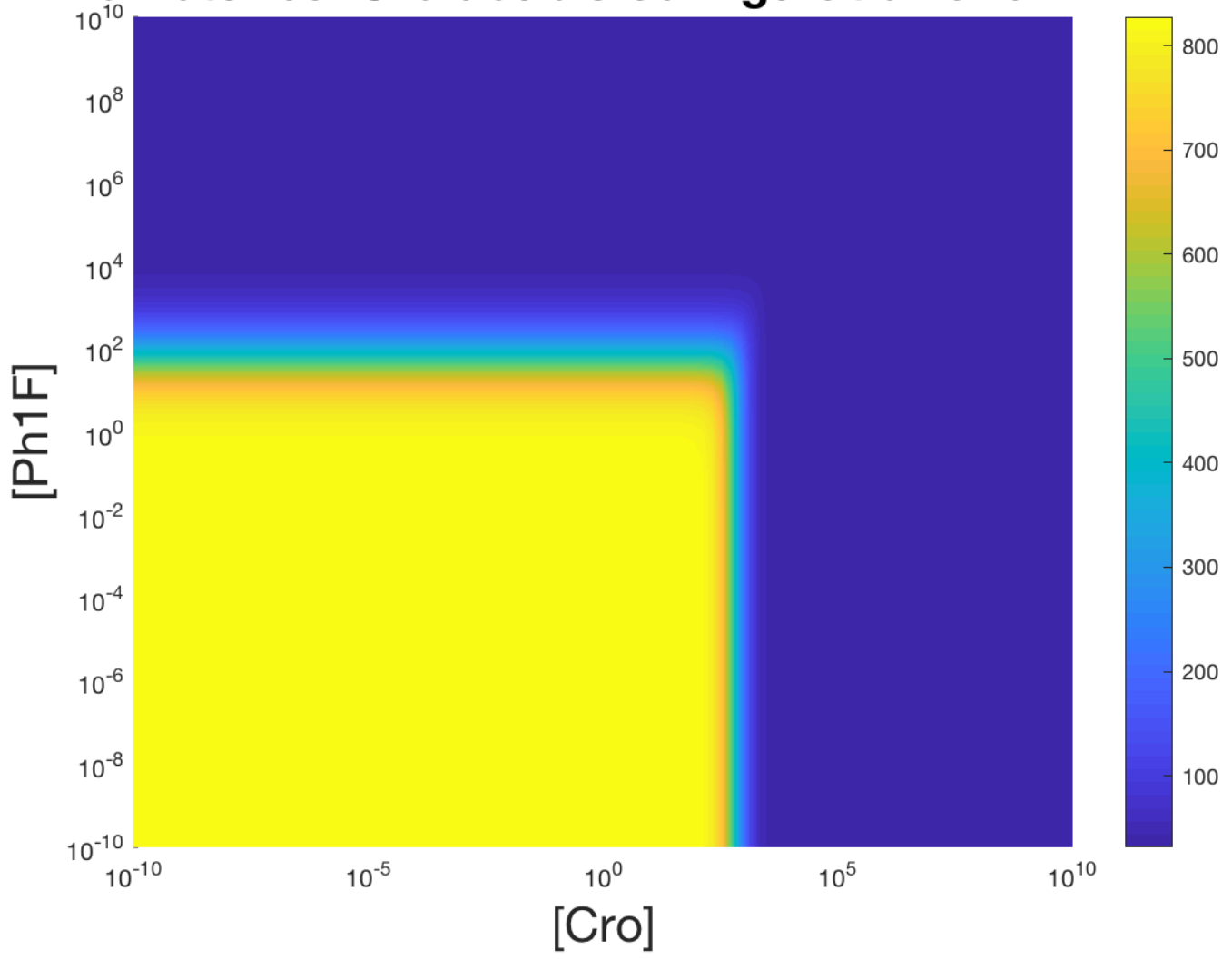
# Promoter 37: Cro double configuration and mTetR



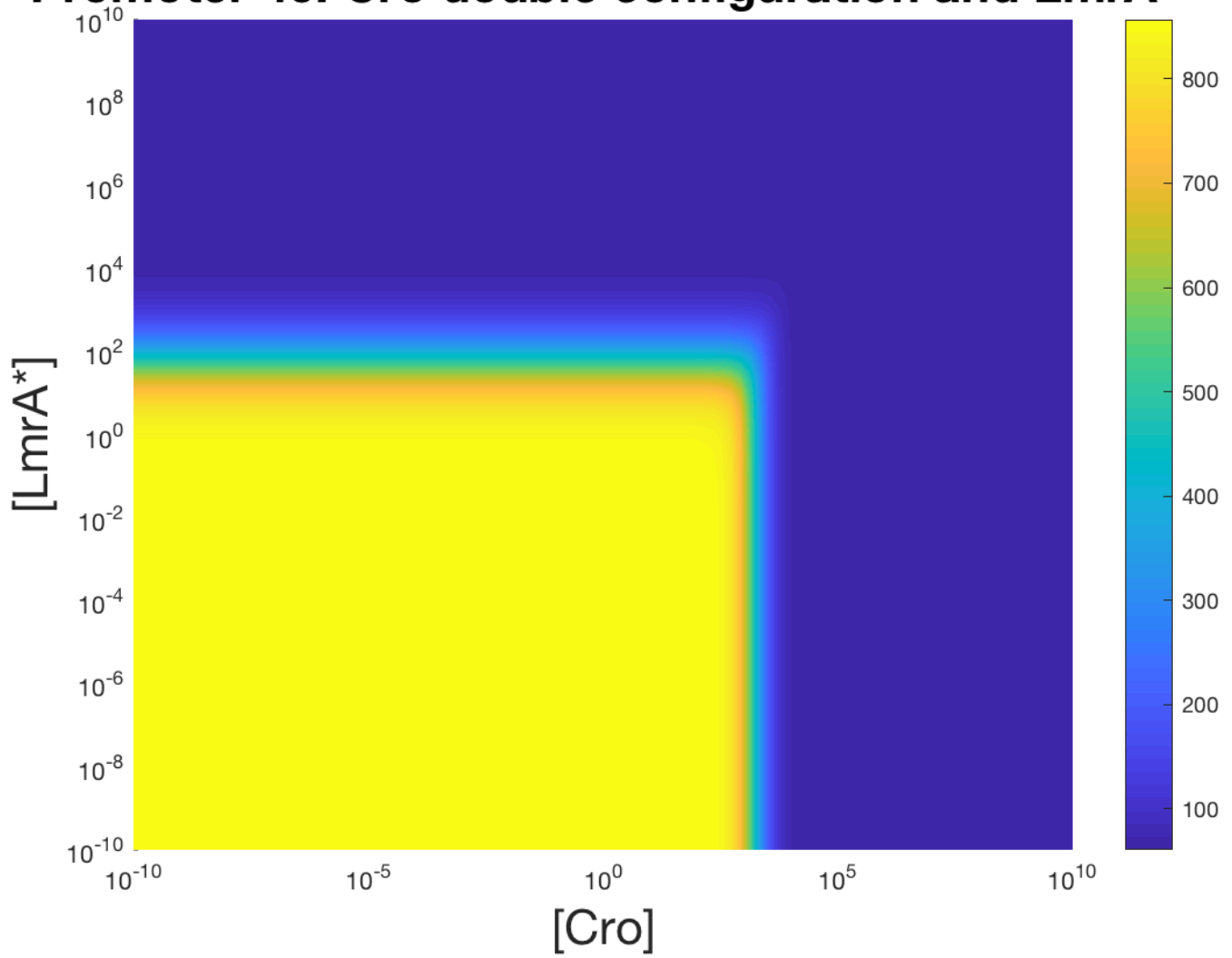
## Promoter 38: Cro double configuration and CymR



# Promoter 39: Cro double configuration and Ph1F

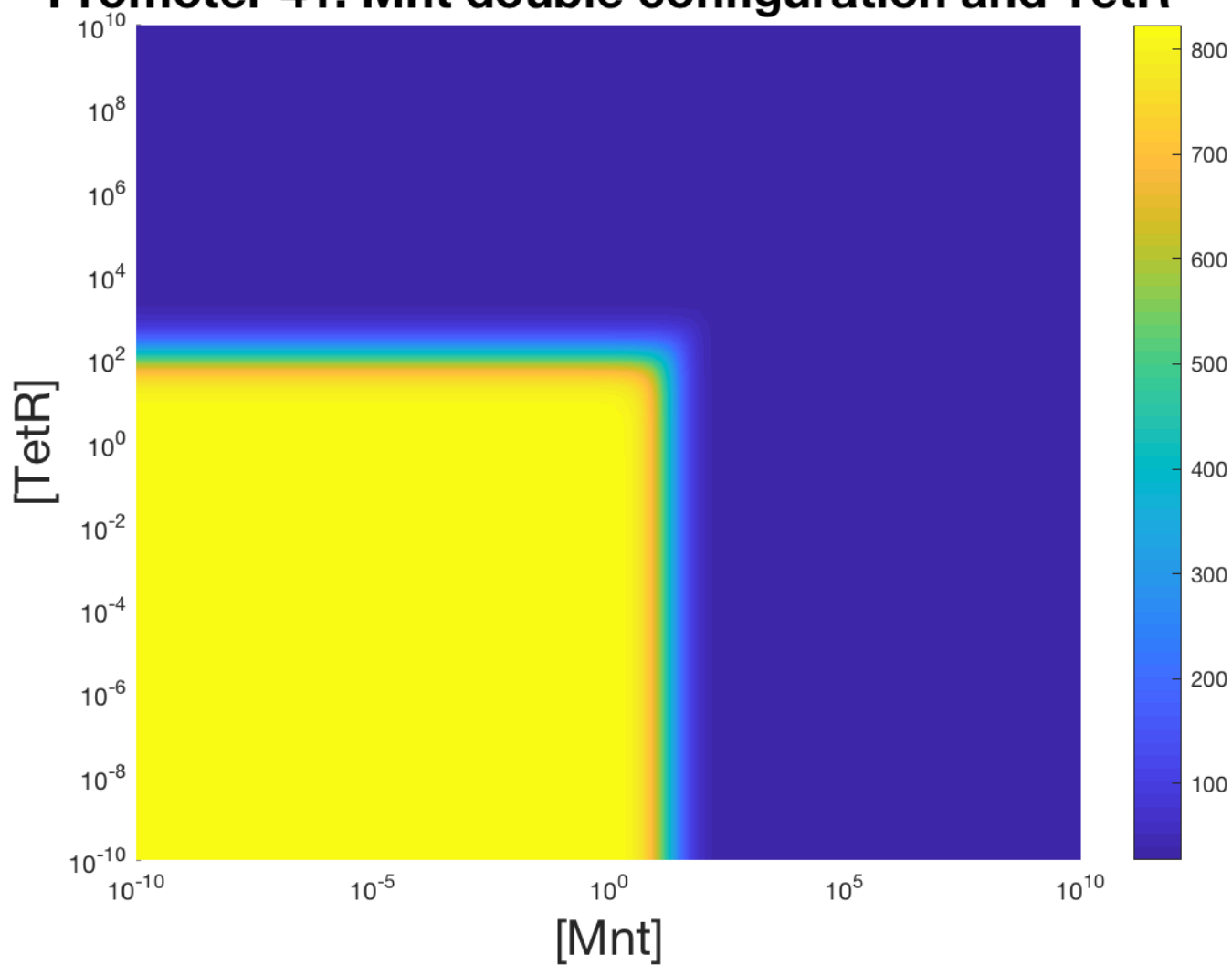


## Promoter 40: Cro double configuration and LmrA\*

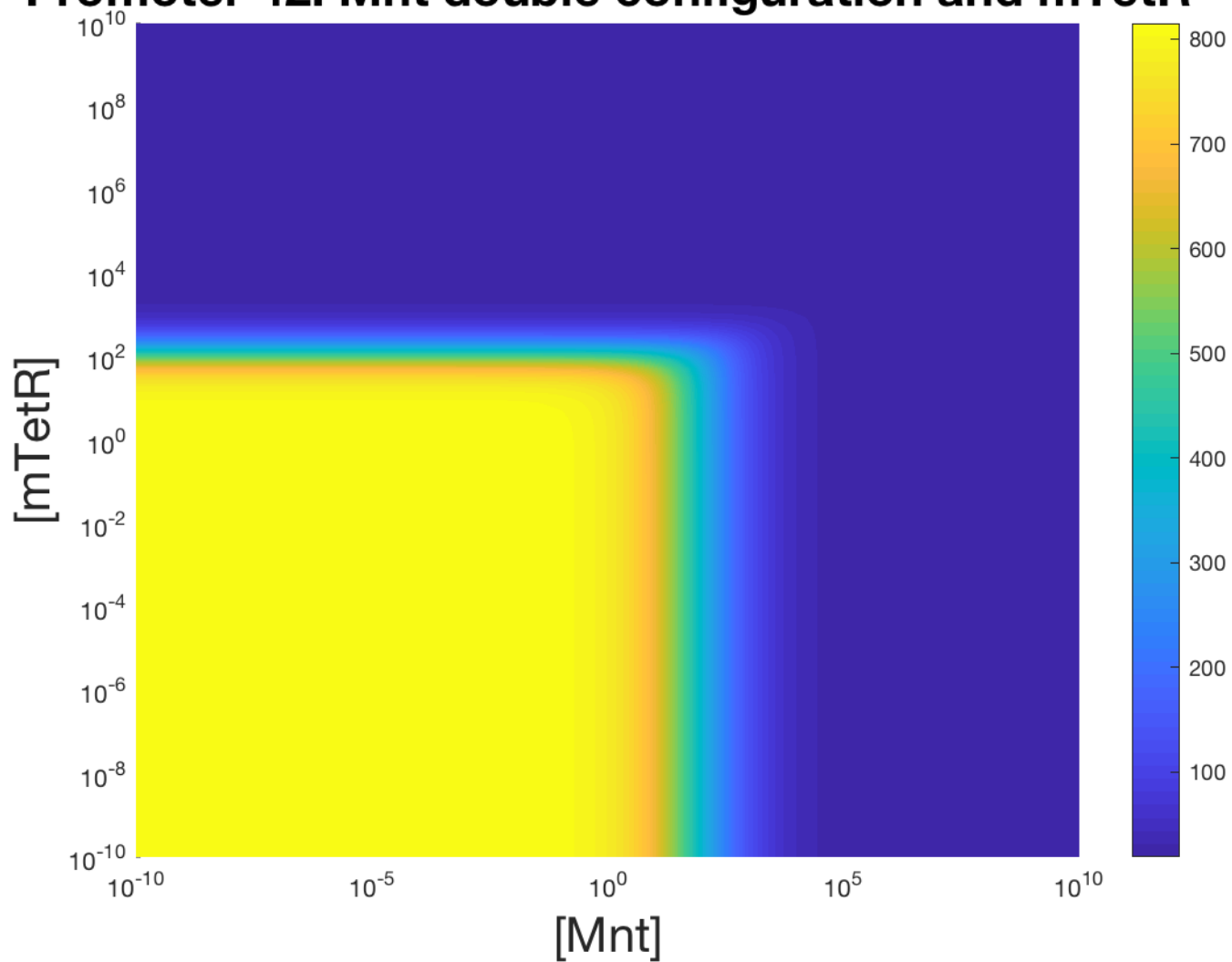




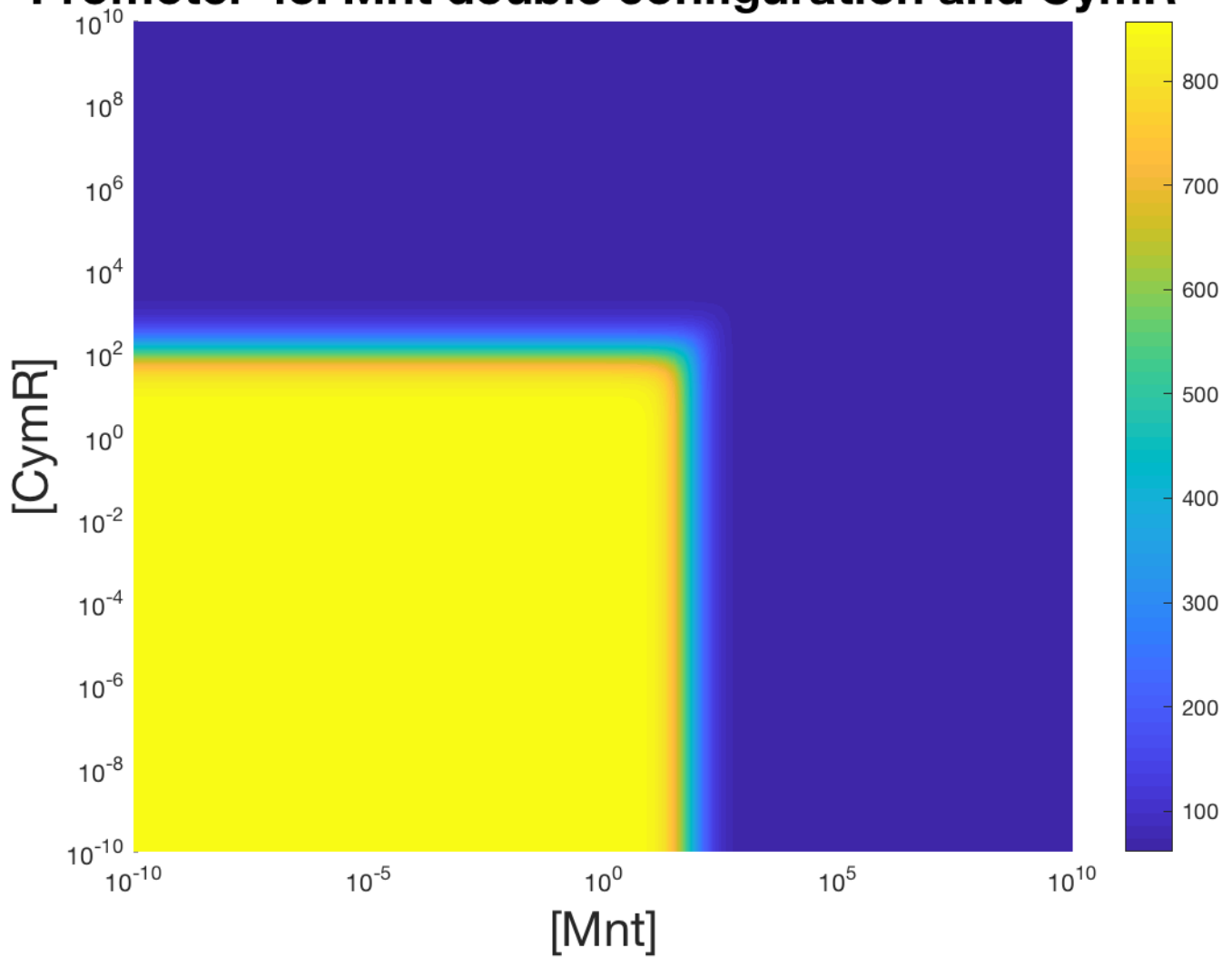
## Promoter 41: Mnt double configuration and TetR



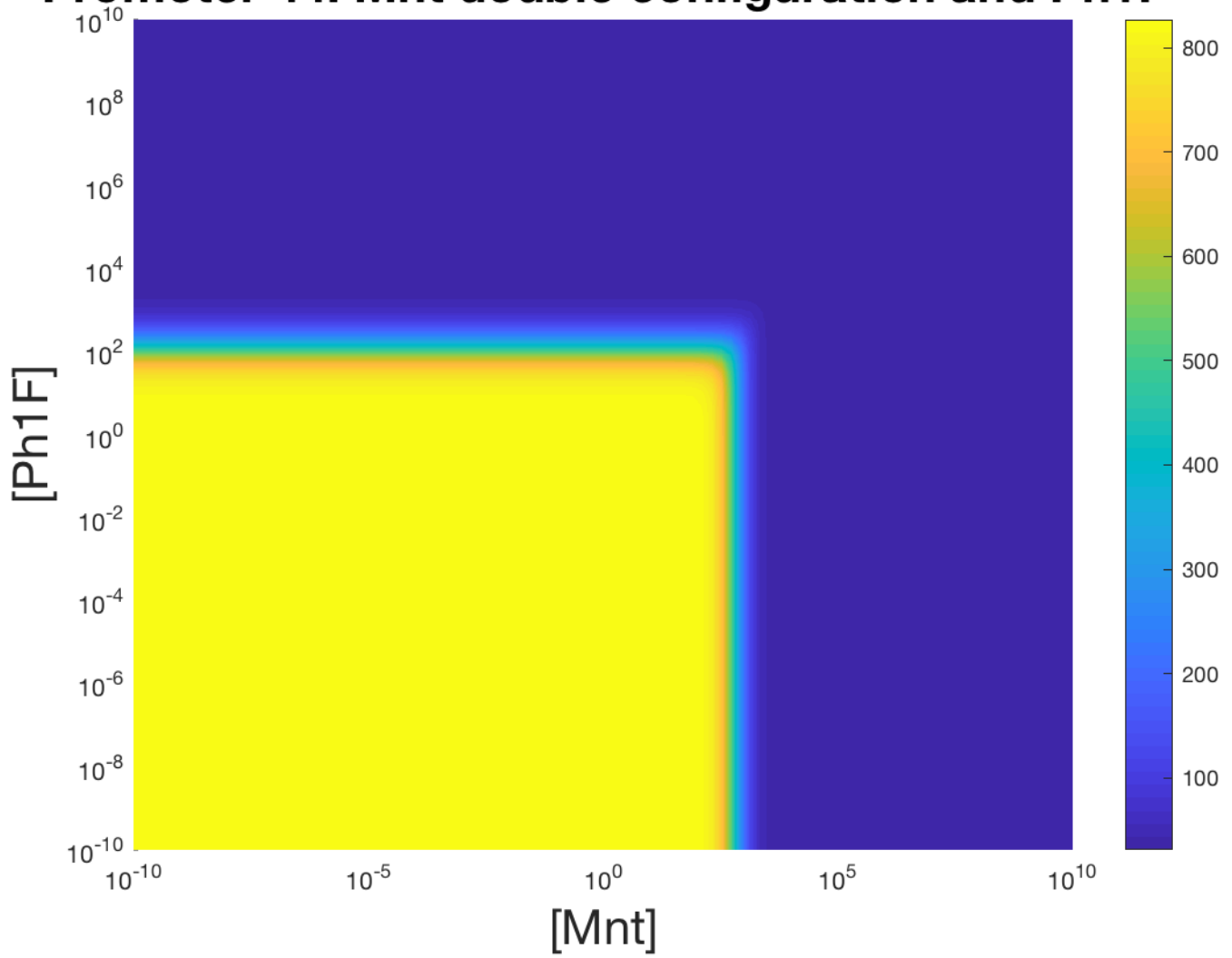
## Promoter 42: Mnt double configuration and mTetR



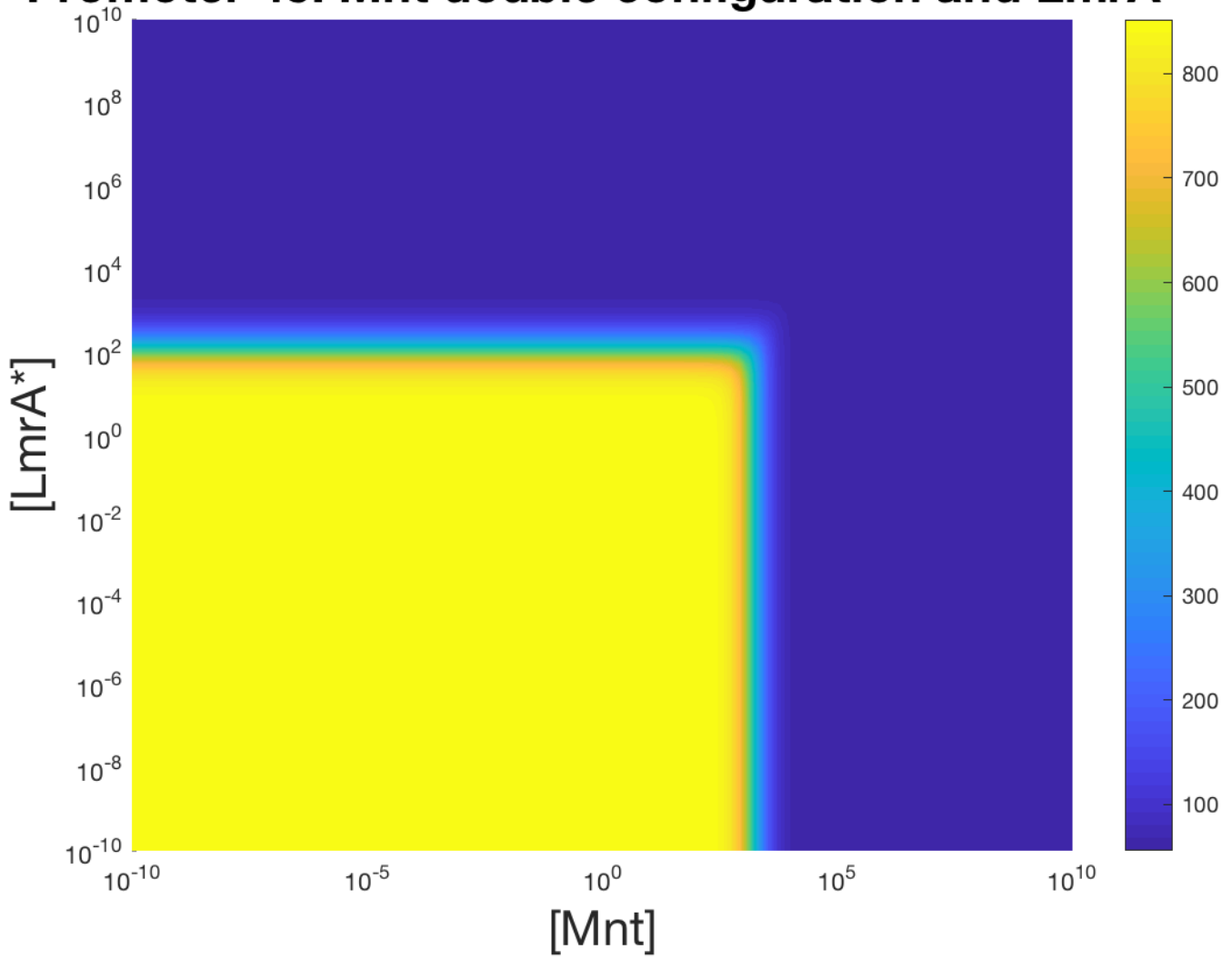
## Promoter 43: Mnt double configuration and CymR



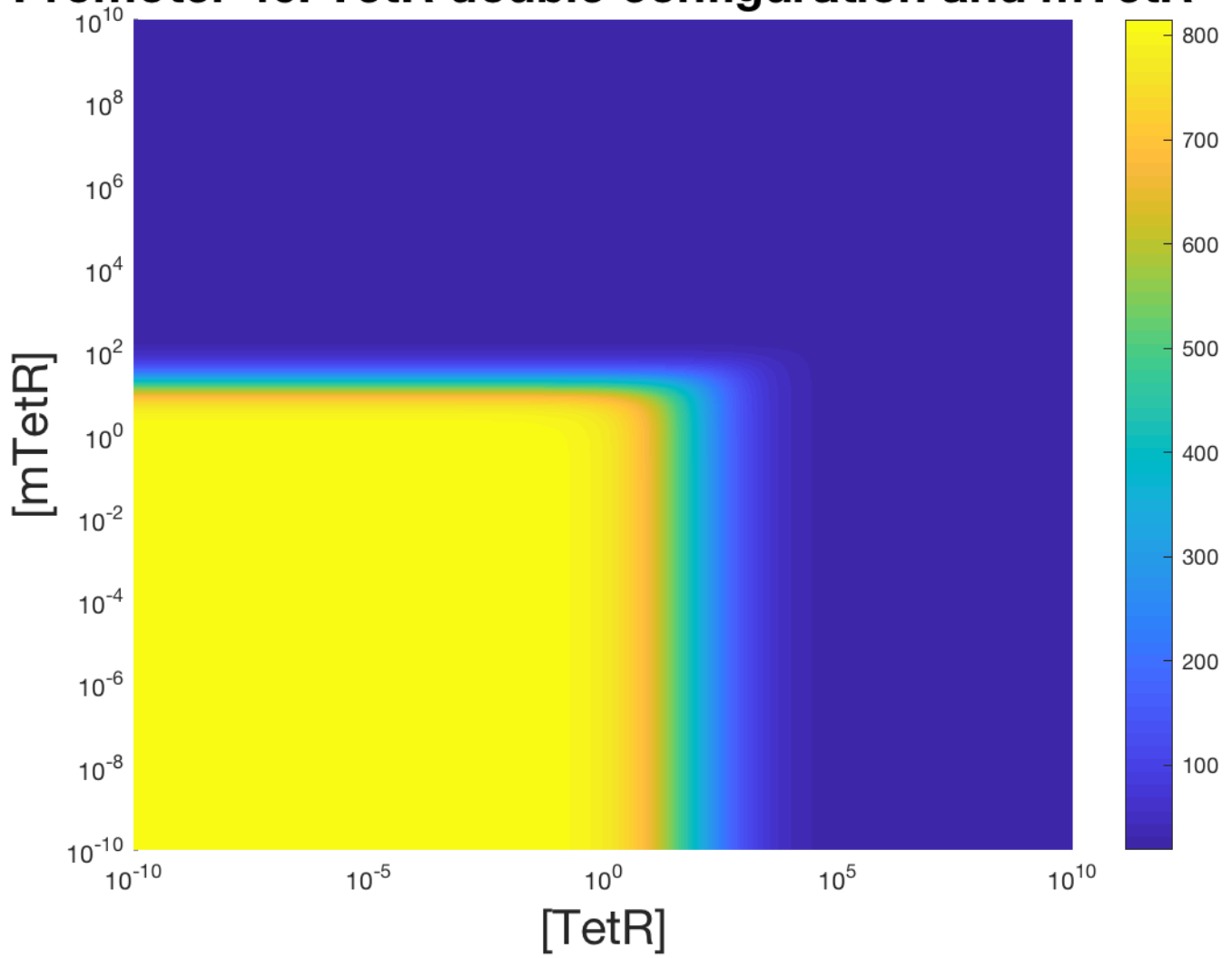
## Promoter 44: Mnt double configuration and Ph1F



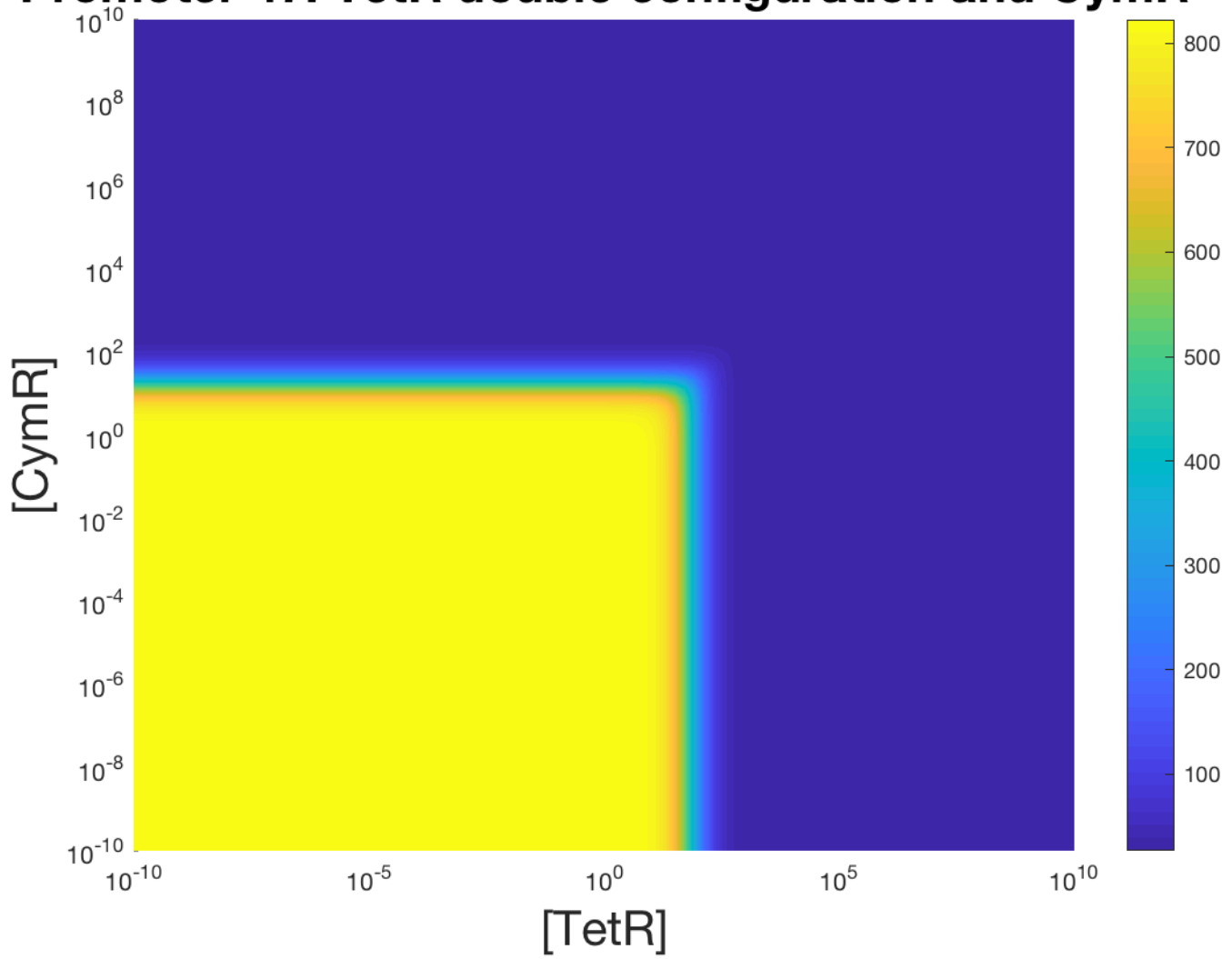
## Promoter 45: Mnt double configuration and LmrA\*



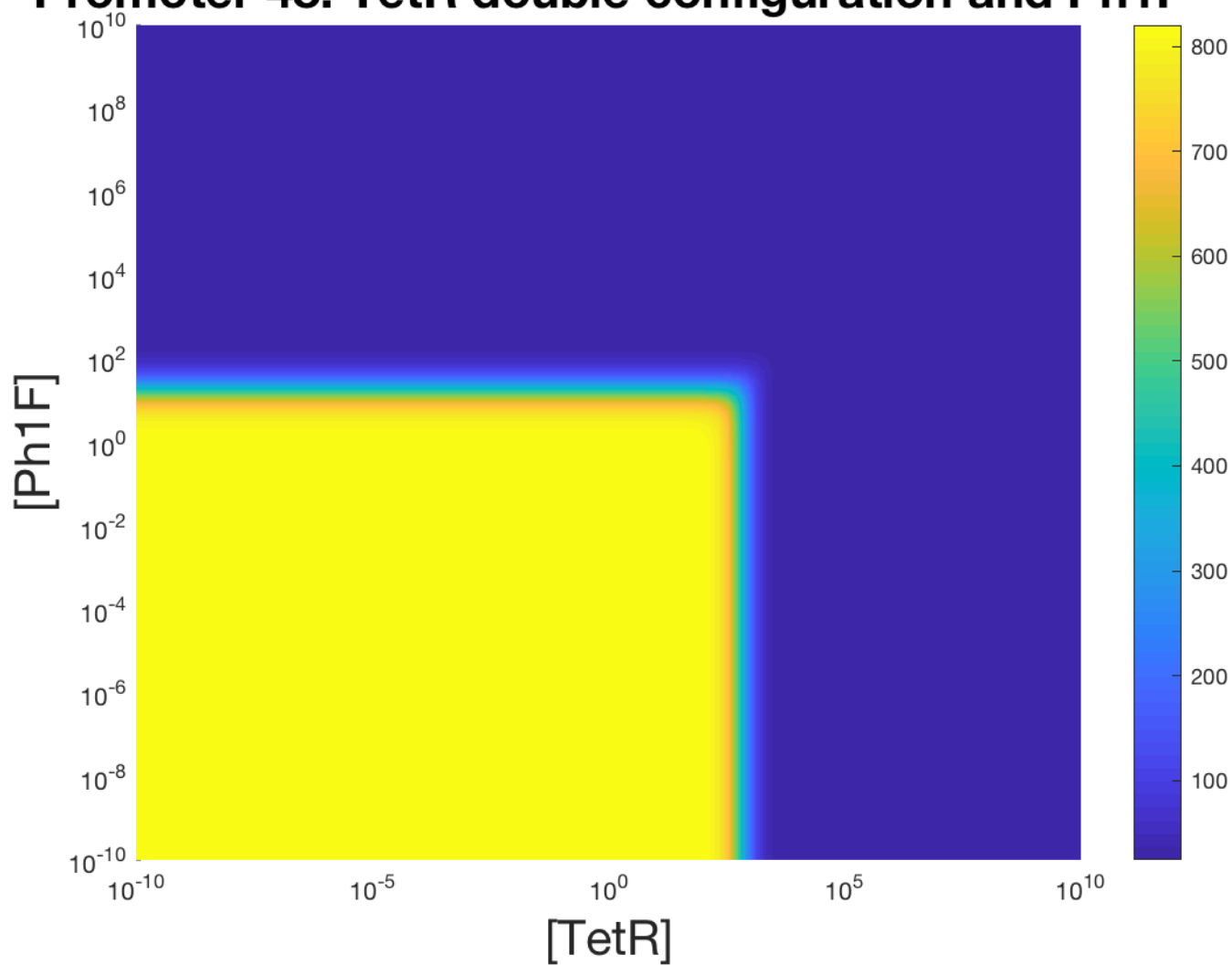
## Promoter 46: TetR double configuration and mTetR



## Promoter 47: TetR double configuration and CymR

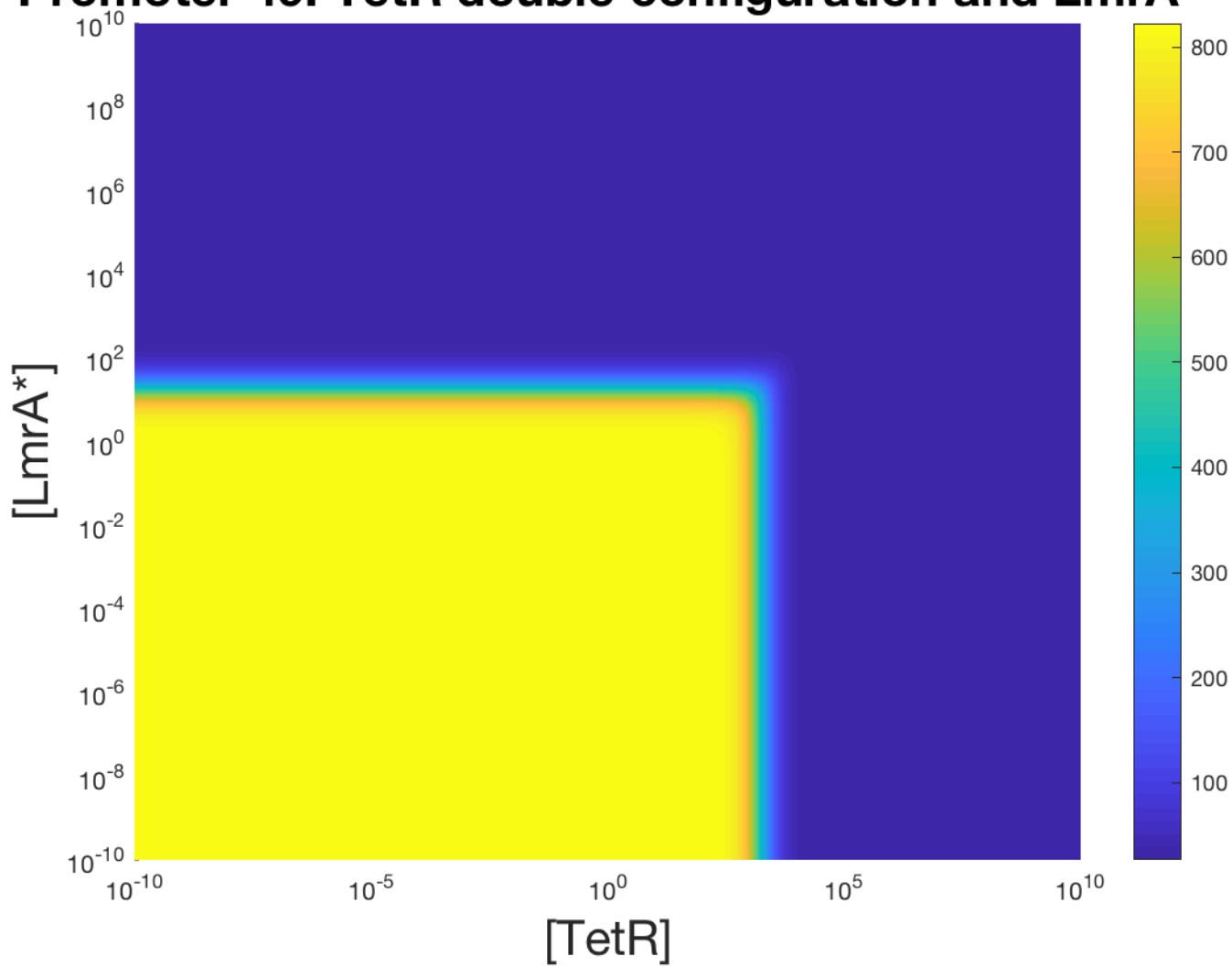


## Promoter 48: TetR double configuration and Ph1F

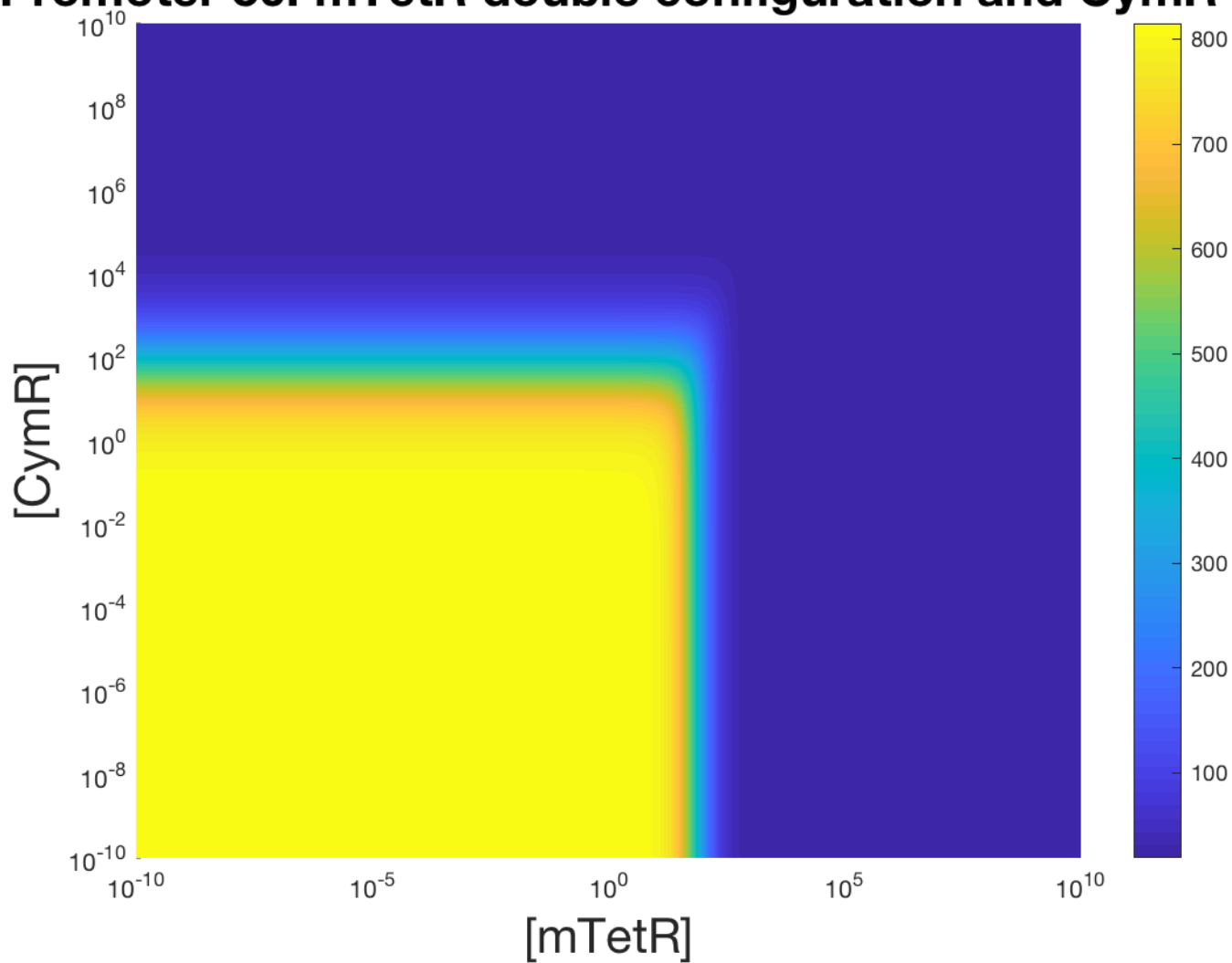




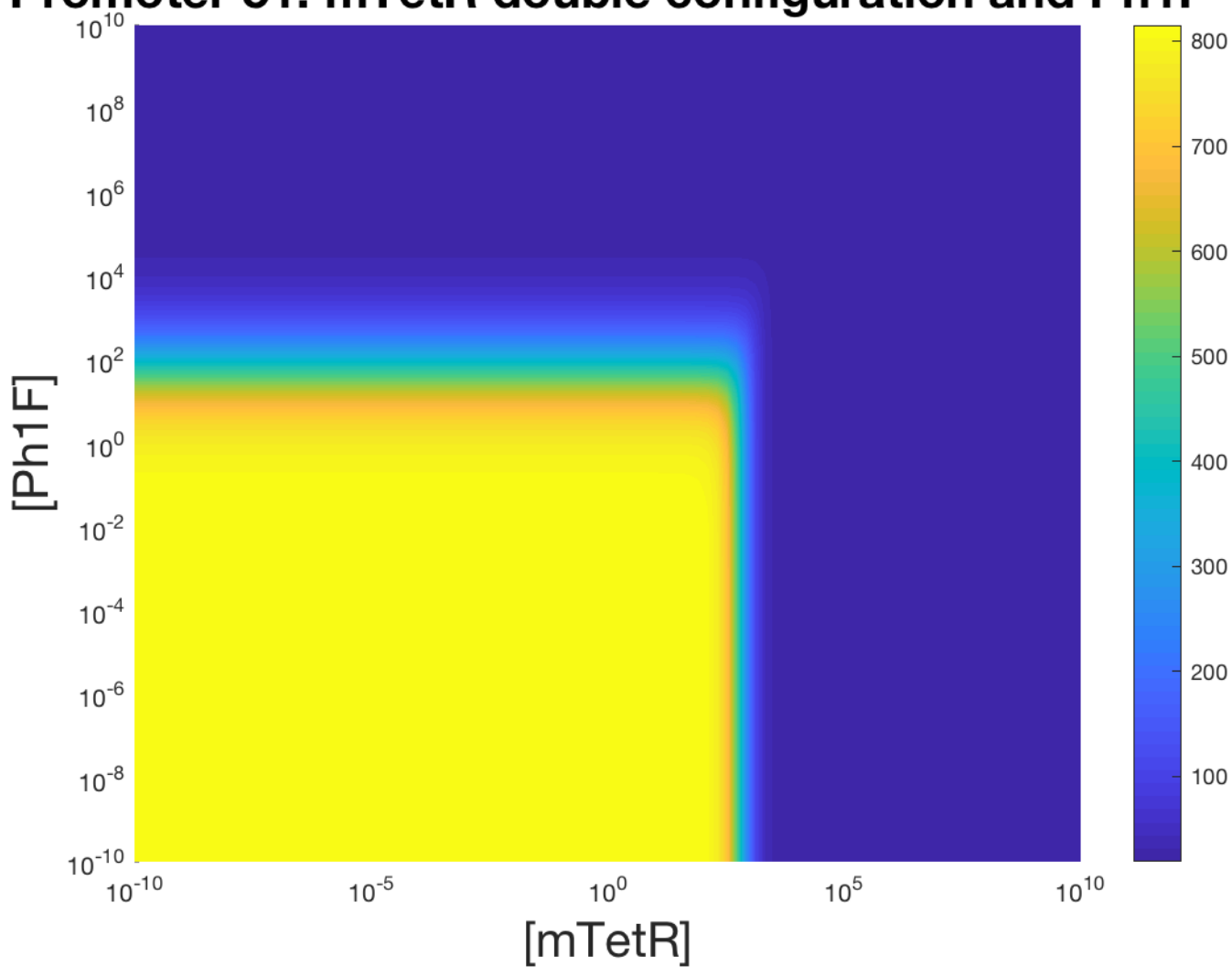
## Promoter 49: TetR double configuration and LmrA\*



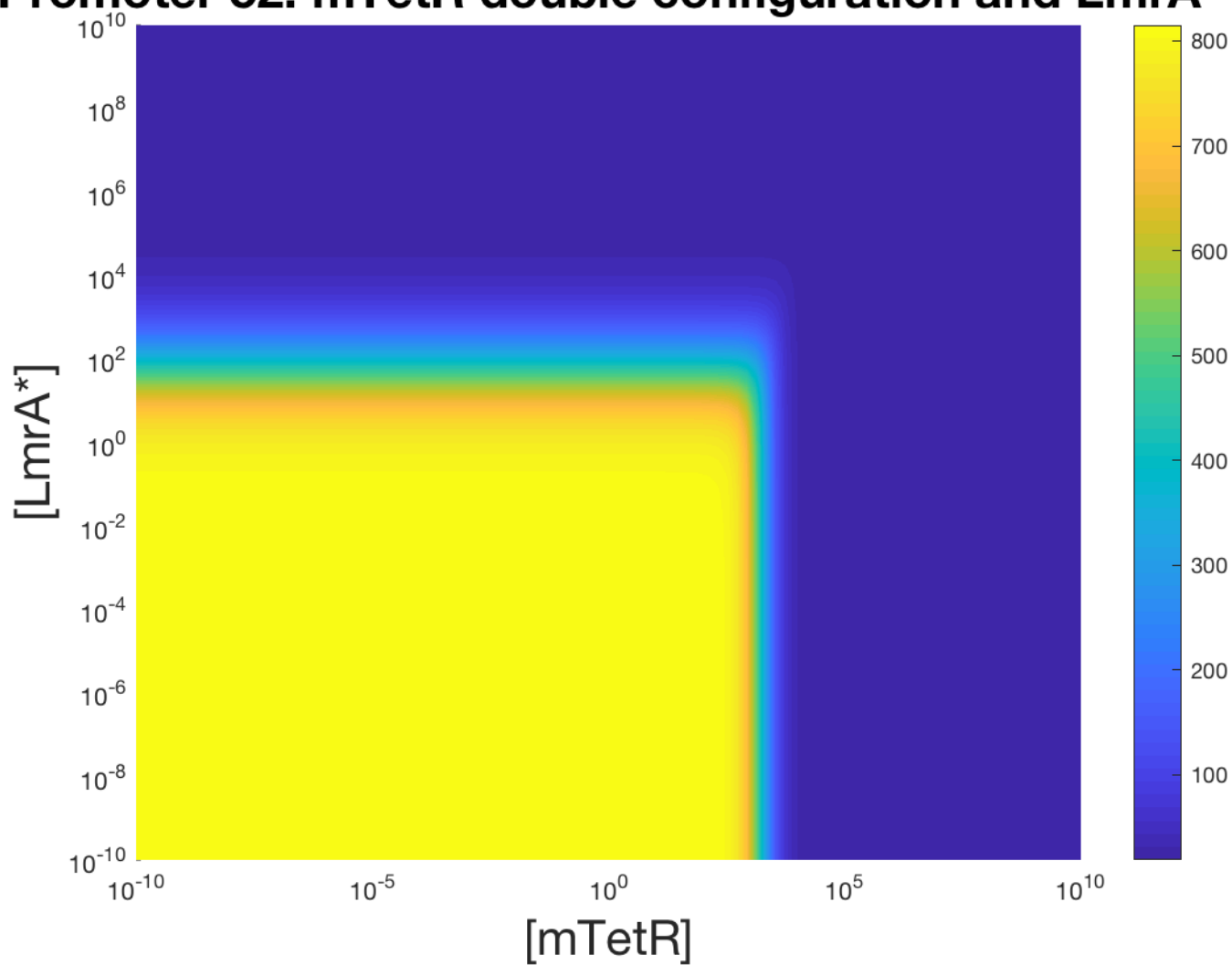
## Promoter 50: mTetR double configuration and CymR



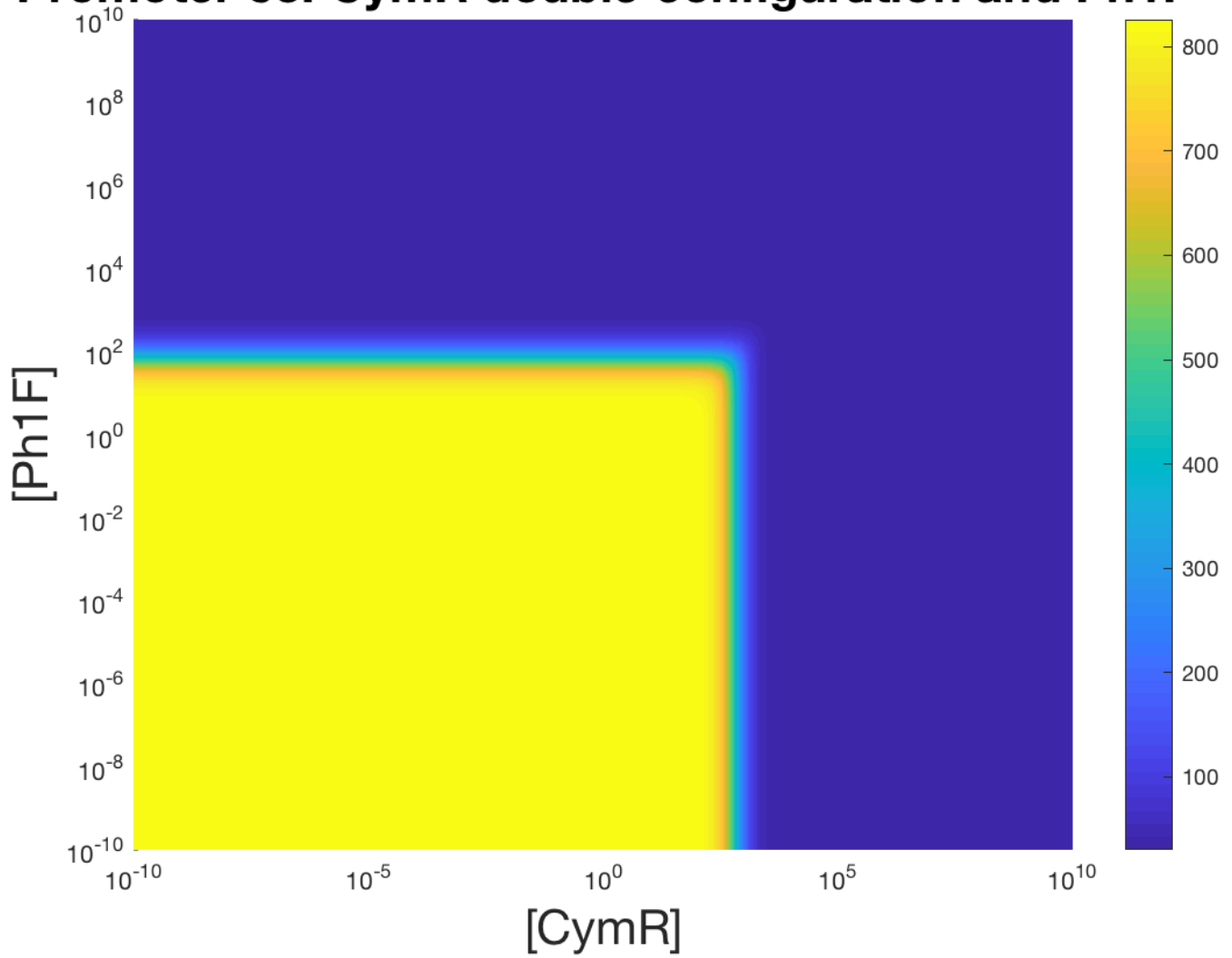
## Promoter 51: mTetR double configuration and Ph1F



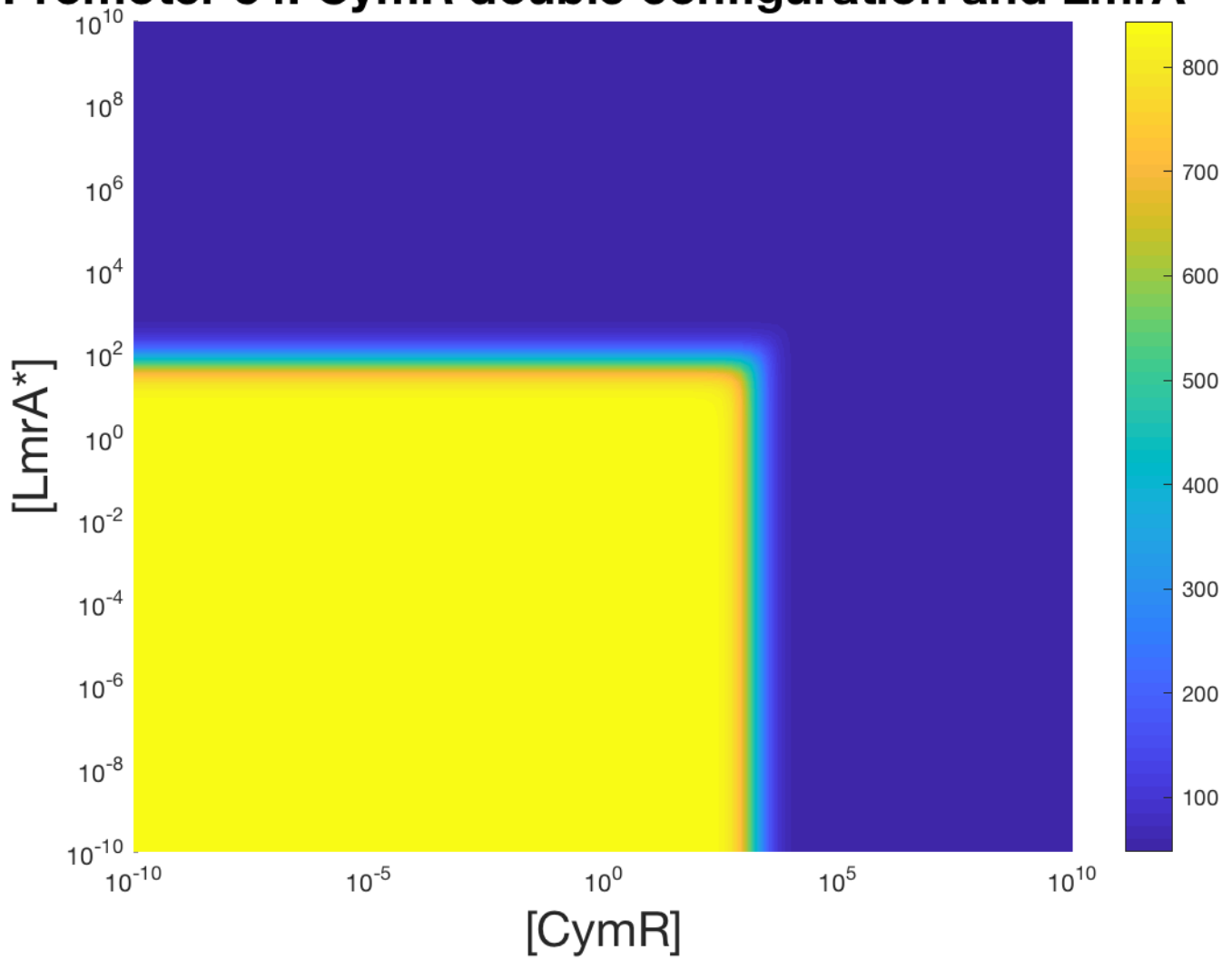
## Promoter 52: mTetR double configuration and LmrA\*



## Promoter 53: CymR double configuration and Ph1F



## Promoter 54: CymR double configuration and LmrA\*



## Promoter 55: Ph1F double configuration and LmrA\*

